

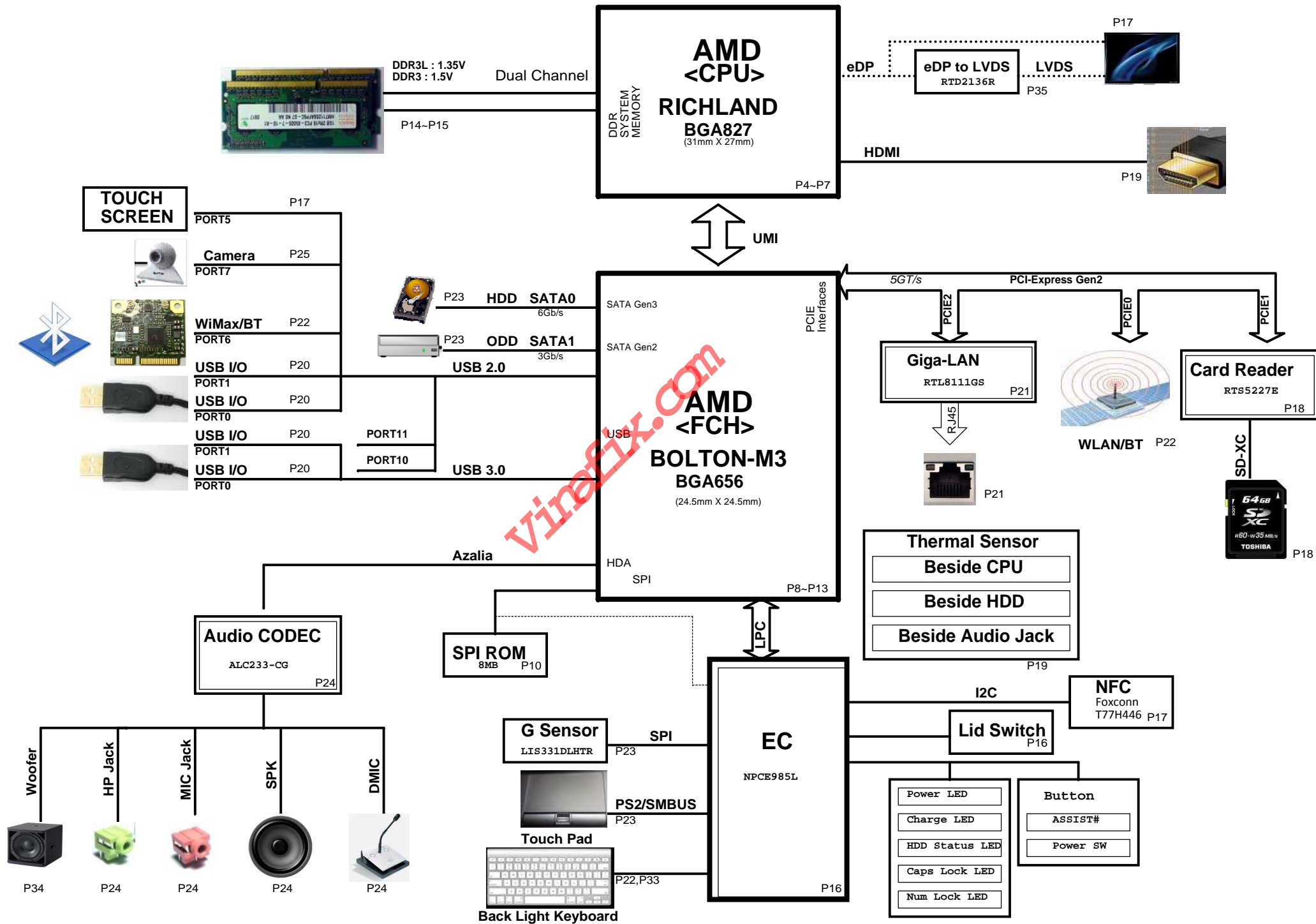
| Page | Title of schematic page     | Rev. | Date |
|------|-----------------------------|------|------|
| 01   | Page List                   | 1A   |      |
| 02   | Block Diagram               | 1A   |      |
| 03   | Change List                 | 1A   |      |
| 04   | FP2 1/4(PEG&UMI)            | 1A   |      |
| 05   | FP2 2/4(DDR3 I/F)           | 1A   |      |
| 06   | FP2 3/4(DP/MISC)            | 1A   |      |
| 07   | FP2 4/4(POWER/GND)          | 1A   |      |
| 08   | FCH 1/6(GPIO/USB/AZ)        | 1A   |      |
| 09   | FCH 2/6(UMI/PCIE/PCI/CLK)   | 1A   |      |
| 10   | FCH 3/6(SATA/VGA/SPI)       | 1A   |      |
| 11   | FCH 4/6(POWER)              | 1A   |      |
| 12   | FCH 5/6(Strap)              | 1A   |      |
| 13   | FCH 6/6(GND)                | 1A   |      |
| 14   | DDR3 DIMM-0-STD             | 1A   |      |
| 15   | DDR3 DIMM-1-STD             | 1A   |      |
| 16   | WPCE985L & FLASH            | 1A   |      |
| 17   | LVDS\TS\NFC                 | 1A   |      |
| 18   | CARD READER(RTS5209)        | 1A   |      |
| 19   | HDMI/THERMAL                | 1A   |      |
| 20   | USB                         | 1A   |      |
| 21   | LAN (RTL8111GS)             | 1A   |      |
| 22   | WLAN/KB-BL                  | 1A   |      |
| 23   | HDD/ODD/G-SENSOR/TP/FAN     | 1A   |      |
| 24   | Audio ALC233-CG             | 1A   |      |
| 25   | LED/PS/DMIC\Camera          | 1A   |      |
| 26   | POWER +VCC_CORE (ISL62771)  | 1A   |      |
| 27   | POWER 3VPCU&RVCC5(TPS51427) | 1A   |      |
| 28   | POWER 1.35VSUS/VTT_MEM      | 1A   |      |
| 29   | POWER +1.1V(G5602R41U)-5A   | 1A   |      |
| 30   | POWER VCC1.2/VCC2.5/Thermal | 1A   |      |
| 31   | POWER(BAT IN / ADA IN/ UL)  | 1A   |      |
| 32   | POWER CHARGER (ISL88731C)   | 1A   |      |
| 33   | HOLE/EMI/KB                 | 1A   |      |
| 34   | eDP to LVDS                 | 1A   |      |
| 35   | IO PORT LIST                | 1A   |      |
|      |                             |      |      |
|      |                             |      |      |
|      |                             |      |      |
|      |                             |      |      |

\* : No mount  
I@ : For native eDP output  
D@ : For eDP to LVDS output



# HKA BLOCK DIAGRAM

02



Change List

**MB\_SCH\_DVT\_001**  
P21 Reserve U30,LR12,LR13,LC21  
Reason : For LAN S5 wake up won't be supported.  
Possible Risk: No.

**MB\_SCH\_DVT\_002**  
P25 Chang R198 and R309 resistor  
Reason : Modify circuit for LED Light  
Possible Risk: No.

**MB\_SCH\_DVT\_003**  
P22 Delete R333 0ohm and add F9(0.35A) fuse  
P22 Delete R468[100K],Q33[2N7002]  
Reason : Modify circuit for KB BL protection.  
Possible Risk: No.

**MB\_SCH\_DVT\_004**  
P9 Change part of Crystal Y1 32.768K.  
Reason : Original part is EOD.  
Possible Risk: No.

**MB\_SCH\_DVT\_005**  
P8 Modify circuit for NFC function.  
Change R865,R866 to R861,R864.  
change NFC\_EN from GPIO166 to GEVENT22#.  
Reason : To enable NFC function.  
Possible Risk: No.

**MB\_SCH\_DVT\_006**  
P16 Reserve diode KD4 for EC\_PWRBTN#.  
P22 Reserve diode D12 for WLAN\_RF\_ON.  
Reason :For cost down.  
Possible Risk: No.

**MB\_SCH\_DVT\_007**  
P20 Add choke for USB ports.  
P25 Add choke for Camera USB interface.  
Reason :EMI issue.  
Possible Risk: No.

**MB\_SCH\_DVT\_008**  
P7,P11 Change capacitors 22U X5R 0805 to 22U X5R 0603.  
Reason :To enlarge the distance between Cap. and Keyboard.  
Possible Risk: No.

**MB\_SCH\_DVT\_009**  
P33 Change FCH NUT shape.  
Reason :To fit screw's size.  
Possible Risk: No.

**MB\_SCH\_DVT\_010**  
P16 Add KR42 10K ohm.  
Reason :To fit LVDS panel power sequence.  
Possible Risk: No.

**MB\_SCH\_DVT\_011**  
P8 Add SMBUS path from FCH to RTD2136R.  
Reason :Reserve the way to flash RTD2136R eFuse.  
Possible Risk: No.

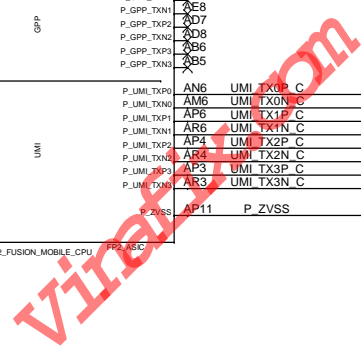
**MB\_SCH\_DVT\_012**  
P9,P21 Change Capacitors for XTAL Y2, LY1.  
Reason :For more precisely frequency of XTAL.  
Possible Risk: No.

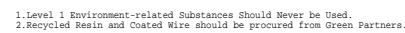
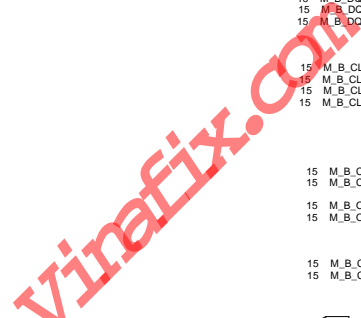
**MB\_SCH\_DVT\_013**  
P19 Change Resistor of HDMI signals resistors.  
Reason :For better HDMI signal quality.  
Possible Risk: No.

**POWER**  
  
2A-P1  
Change PC580 and PC599 from 0.047u to 0.068 for fine tune IC response  
  
2A-P2  
add PC617, PC618, PC619, PC620 for meet AMD SPEC of ripple  
  
2A-P3  
change PR58 from 255k to 196k for fine tune OCP  
  
2A-P4  
change from 3.75k to 3K for fine tune OCP

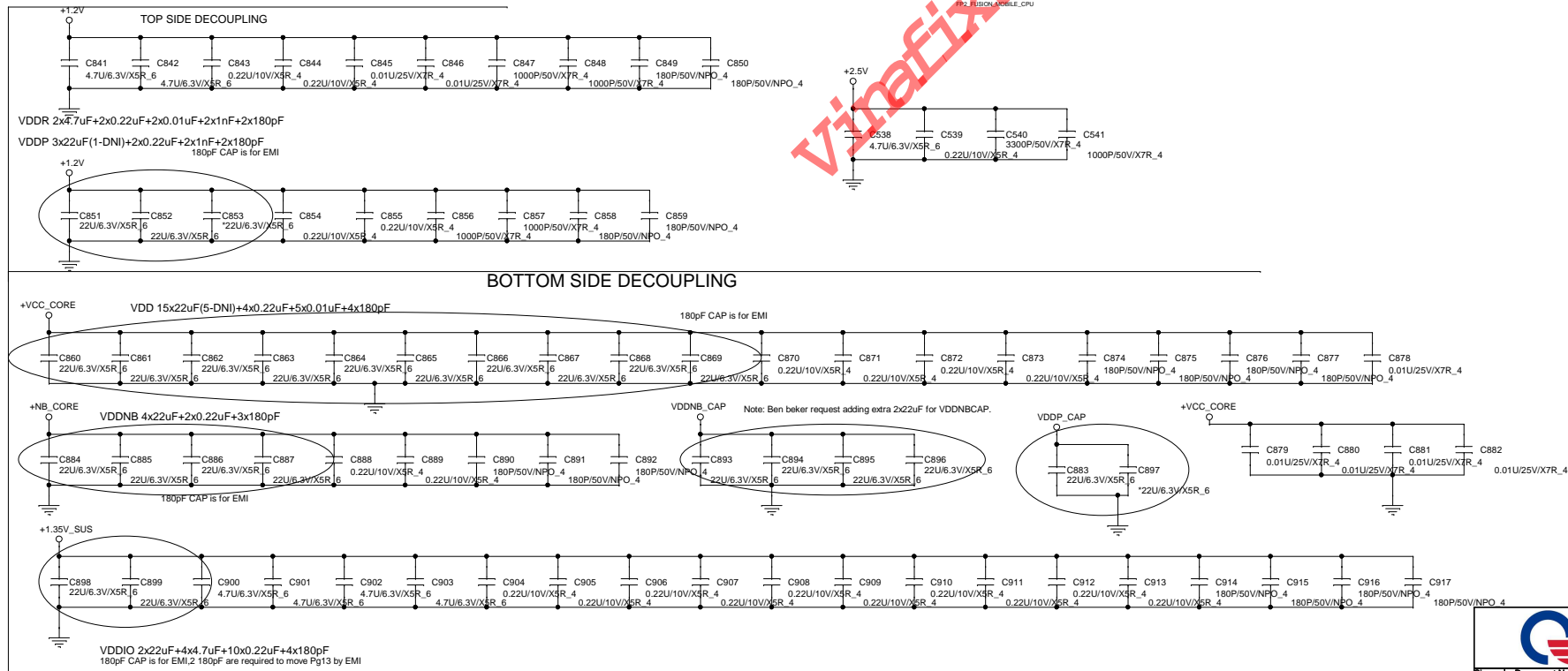
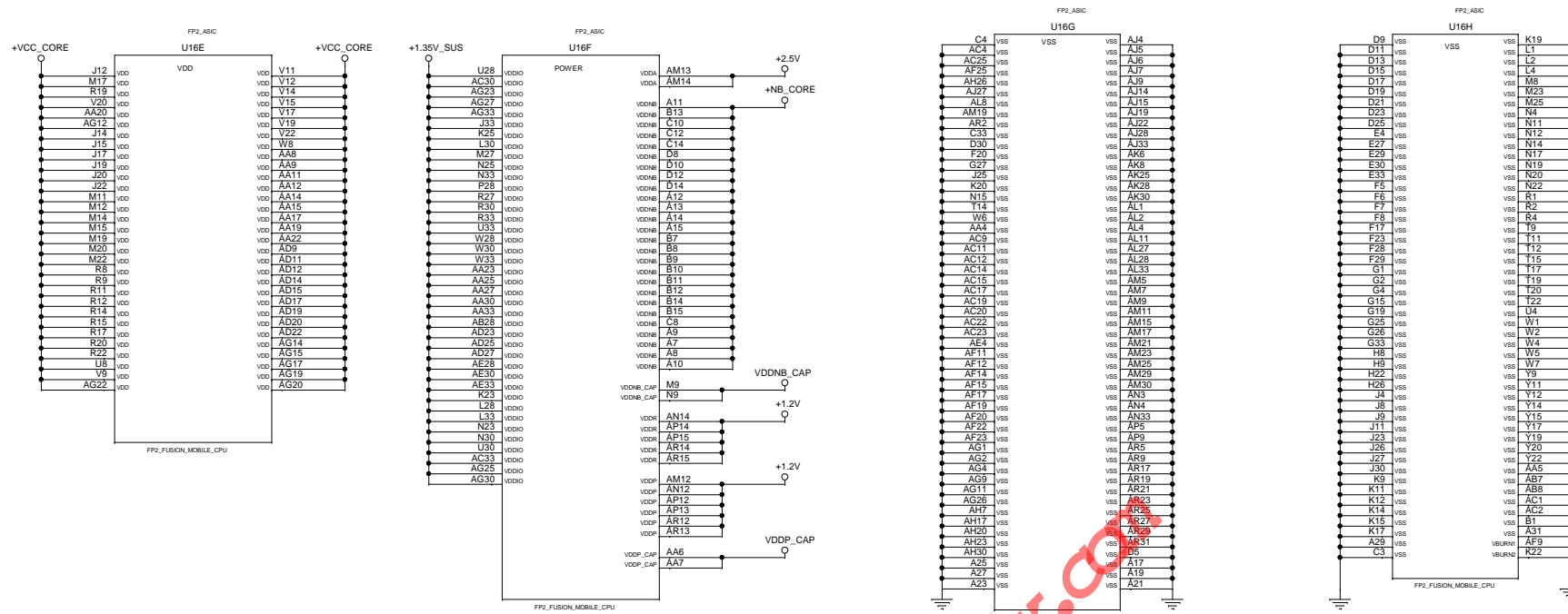
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1.Level 1 Environment-related Substances Should Never be Used.  
2.Recycled Resin and Coated Wire should be procured from Green Partners.

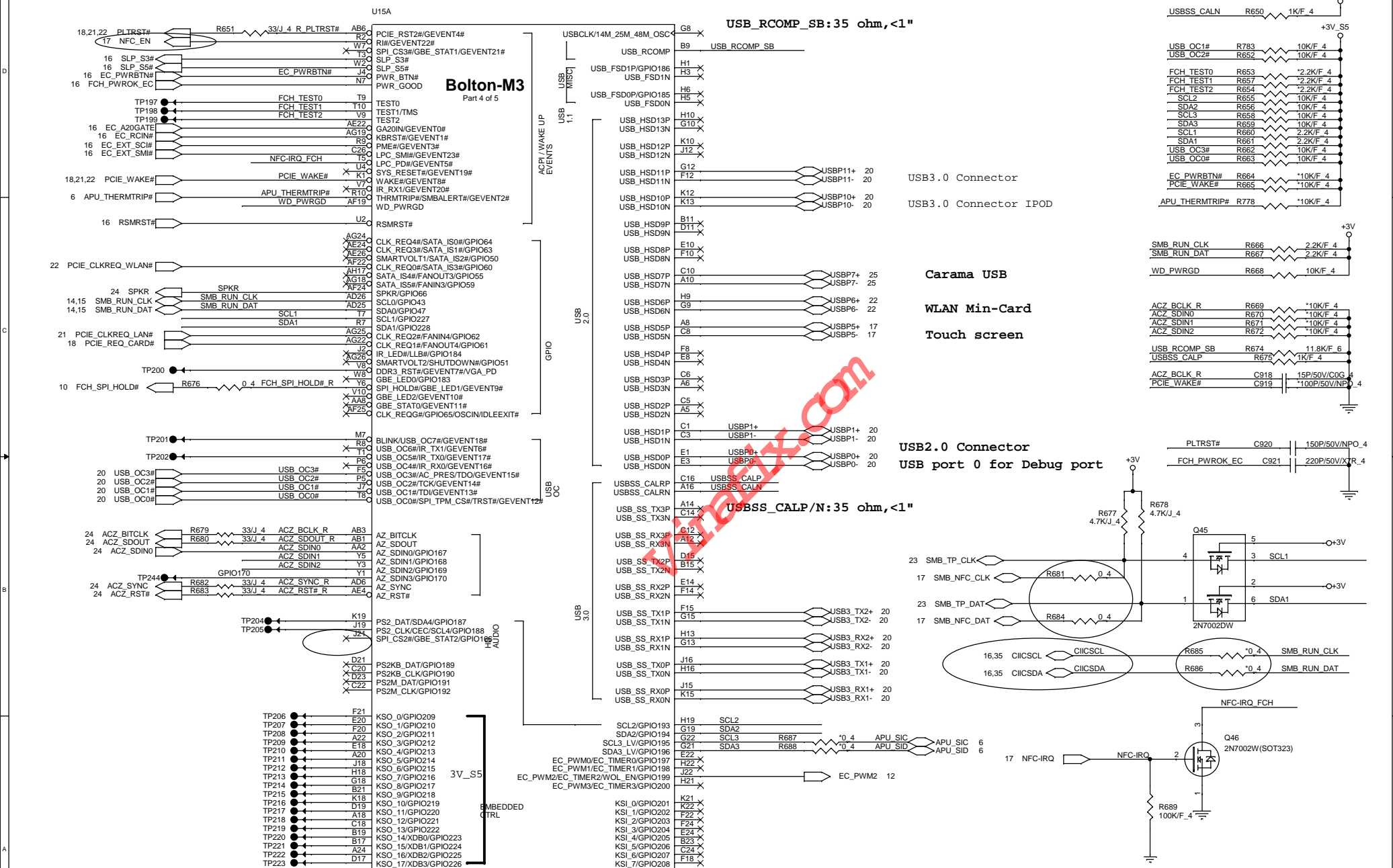






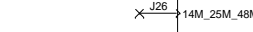
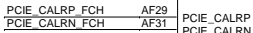




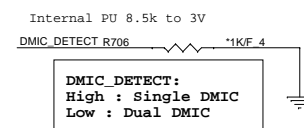
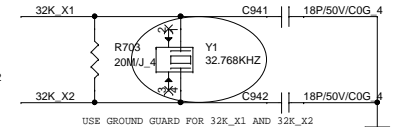
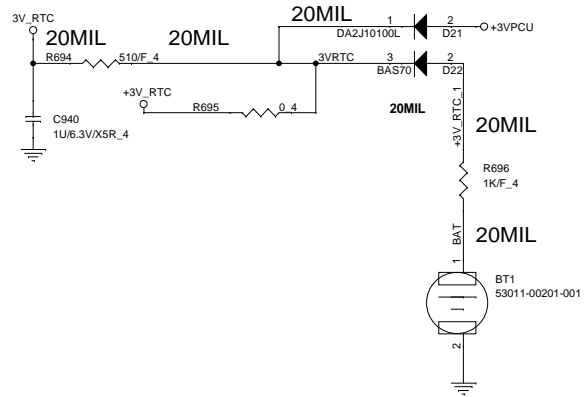
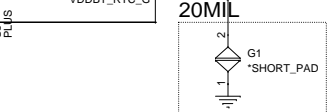
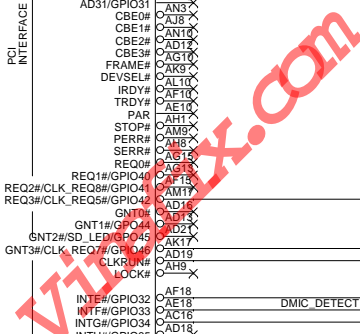




PCIE\_CALP/N\_FCH:55 ohm,&lt;1"



Part 1 of 5



1.Level 1 Environment-related Substances Should Never be Used.  
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SATA HDD

SATA ODD

SATA\_CALP/N:35 ohm,&lt;1"

25 SATA\_ACT#

TP232 ● AH16  
TP233 ● AM15  
TP234 ● AJ16

TP235 ● AK15  
TP236 ● AN16  
TP237 ● AL16

TEMPIN0 K6  
TEMPIN1 K5  
TEMPIN2 K3  
TEMPIN3 M6

TEMPIN0/GPIO171  
TEMPIN1/GPIO172  
TEMPIN2/GPIO173  
TEMPIN3/TALERT#GPI0174

## Bolton-M3

Part 2 of 5

SD\_CLK/CLK\_2/GPIO73  
SD\_CMD/LOAD\_2/GPIO74  
SD\_CD#/GPIO75  
SD\_WP/GPIO76  
SD\_DATA0/SDAT1\_2/GPIO77  
SD\_DATA1/SDAT0\_2/GPIO78  
SD\_DATA2/GPIO79  
SD\_DATA3/GPIO80

GBE\_COL AC4  
GBE\_CRS AD1  
GBE\_MDCK W10  
GBE\_MDIO AB8  
GBE\_RXCLK AH7  
GBE\_RXD3 AF7  
GBE\_RXD2 AE7  
GBE\_RXD1 AD7  
GBE\_RXD0 AG8  
GBE\_RXCTL/RXDV AD1  
GBE\_RXERR AB7  
GBE\_TXCLK AF9  
GBE\_TXD3 AE8  
GBE\_TXD2 AE6  
GBE\_TXD1 AD6  
GBE\_TXD0 AB9  
GBE\_TXCTL/TXEN AC2  
GBE\_PHY\_PD AA7  
GBE\_PHY\_RST# OW9  
GBE\_PHY\_INTR

V6 FCH\_SPI\_SI  
V5 FCH\_SPI\_SO  
V3 FCH\_SPI\_CLK  
V6 FCH\_SPI\_CS0#  
V1 FCH\_SPI\_WP# R716 0.4 FCH\_SPI\_WP#

VGA\_RED L30  
VGA\_GREEN L32  
VGA\_BLUE M29

VGA\_HSYNC/GPO68  
VGA\_VSYNC/GPO69

VGA\_DDC\_SDA/GPO70  
VGA\_DDC\_SCL/GPO71

VGA\_DAC\_RSET K31

AUX\_VGA\_CH\_P V28  
AUX\_VGA\_CH\_N V29

AUXCAL U28

ML\_VGA\_L0# T33  
ML\_VGA\_L0N T32  
ML\_VGA\_L1# T28  
ML\_VGA\_L1N T37  
ML\_VGA\_L2# R36  
ML\_VGA\_L2N P29  
ML\_VGA\_L3# P28  
ML\_VGA\_L3N P28

ML\_VGA\_HPD/GPIO229

VIN0/GPIO175  
VIN1/GPIO176  
VIN2/SDAT1\_1/GPIO177  
VIN3/SDAT0\_1/GPIO178  
VIN4/SLOAD\_1/GPIO179  
VIN5/SCLK\_1/GPIO180  
VIN6/GBE\_STAT3/GPIO181  
VIN7/GBE\_LED3/GPIO182

GPIO175  
GPIO176  
GPIO177

BOARD ID0  
BOARD ID1  
BOARD ID2  
BOARD ID3  
BOARD ID4

GPIO175  
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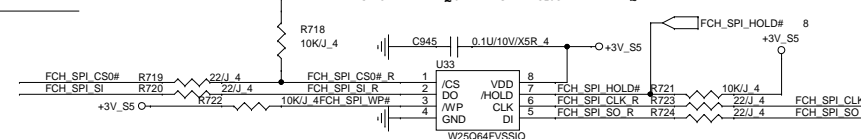
GPIO175  
GPIO176  
GPIO177

GPIO175  
GPIO176  
GPIO177

GPIO175  
GPIO176  
GPIO177

## FCH SPI (CLG)

W25X64FVSSIQ: AKE3EFP0N07 For Quad IO



Resistors close to SPI ROM

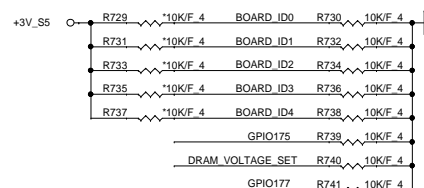
16 F\_CS0#\_PCH R725 22/J 4 FCH\_SPI\_CS0# R  
16 F\_SDI\_PCH R726 22/J 4 FCH\_SPI\_SI R  
16 SCK\_PCH R727 22/J 4 FCH\_SPI\_CLK R  
16 SDO\_PCH R728 22/J 4 FCH\_SPI\_SO R

For NPCE885L Using

| SIZE    | Board ID0 |
|---------|-----------|
| HKA 14" | 0         |
| HKB 15" | 1         |

| I/F  | Board ID1 |
|------|-----------|
| eDP  | 0         |
| LVDS | 1         |

| CPU | Board ID2 | Board ID3 |
|-----|-----------|-----------|
| A4  | 0         | 0         |
| A6  | 0         | 1         |
| A8  | 1         | 0         |
| A10 | 1         | 1         |

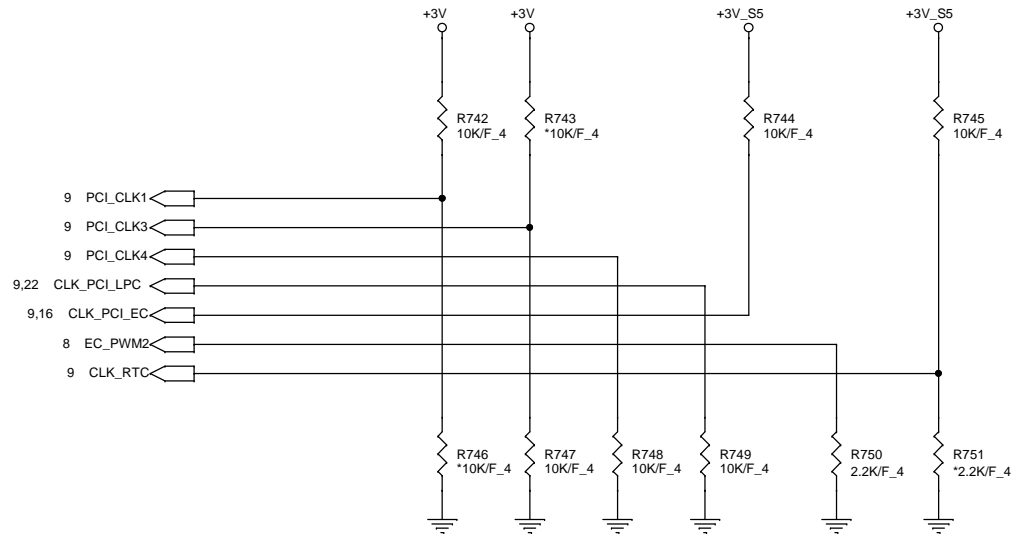




## STRAPS PINS



OVERLAP COMMON PADS WHERE  
POSSIBLE FOR DUAL-OP RESISTORS.



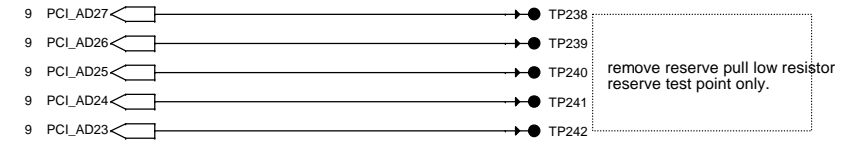
## REQUIRED STRAPS

|           | ----- | PCI_CLK1                      | ----- | PCI_CLK3                            | PCI_CLK4   | (LPCCLK0)<br>CLK_PCI_LPC   | (LPCCLK1)<br>CLK_PCI_EC      | EC_PWM2            | CLK_RTC                             |
|-----------|-------|-------------------------------|-------|-------------------------------------|--|----------------------------|------------------------------|--------------------|-------------------------------------|
| PULL HIGH | ----- | ALLOW<br>PCIe Gen2<br>DEFAULT | ----- | USE<br>DEBUG<br>STRAP               | Reserved   | AMD internal EC<br>ENABLED | CLKGEN<br>ENABLED<br>DEFAULT | LPC ROM            | S5 PLUS MODE<br>DISABLED<br>DEFAULT |
| PULL LOW  | ----- | FORCE<br>PCIe Gen1            | ----- | IGNORE<br>DEBUG<br>STRAP<br>DEFAULT | Required<br>setting for<br>integrated<br>clock mode<br>DEFAULT | EC<br>DISABLED<br>DEFAULT  | CLKGEN<br>DISABLED           | SPI ROM<br>DEFAULT | S5 PLUS MODE<br>enable              |

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## DEBUG STRAPS

FCH has 15K Internal Pull Up for PCI\_AD[27:23]



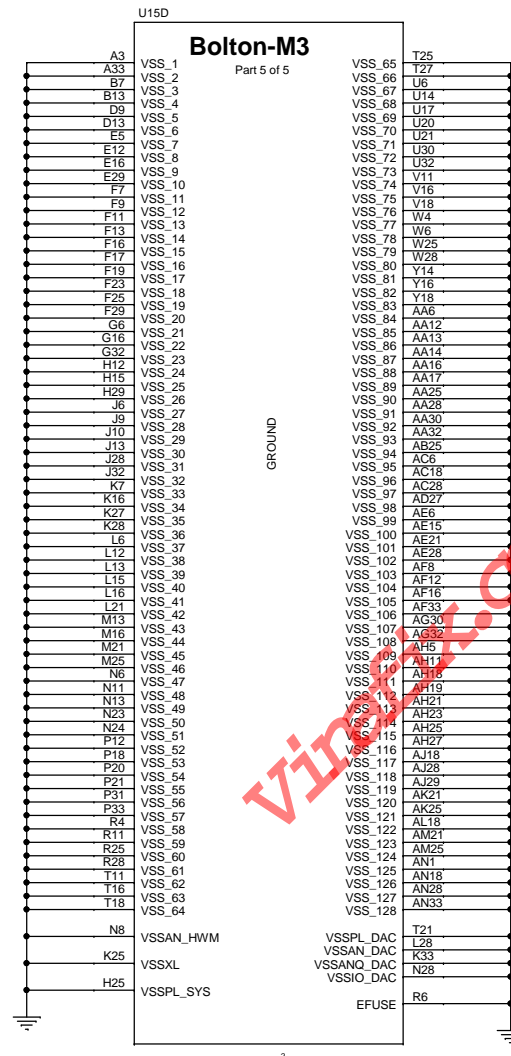
|           | PCI_AD27                      |  | PCI_AD25                        | PCI_AD24                                  | PCI_AD23                               |
|-----------|-------------------------------|--|---------------------------------|---|--|
| PULL HIGH | USE PCI<br>PLL<br><br>DEFAULT |  | normal<br>REFCLK<br><br>DEFAULT | USE DEFAULT<br>PCIe STRAPS<br><br>DEFAULT | DISABLE PCI<br>MEM BOOT<br><br>DEFAULT |
| PULL LOW  | BYPASS<br>PCI PLL             |  | Inverted<br>REFCLK              | USE EEPROM<br>PCIe STRAPS                 | ENABLE PCI<br>MEM BOOT                 |



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PROJECT : HKB

|       |                         |                |
|-------|-------------------------|----------------|
| Size  | Document Number         | Rev            |
|       | FCH 5/6(Strap)          | 1A             |
| Date: | Tuesday, April 23, 2013 | Sheet 12 of 36 |

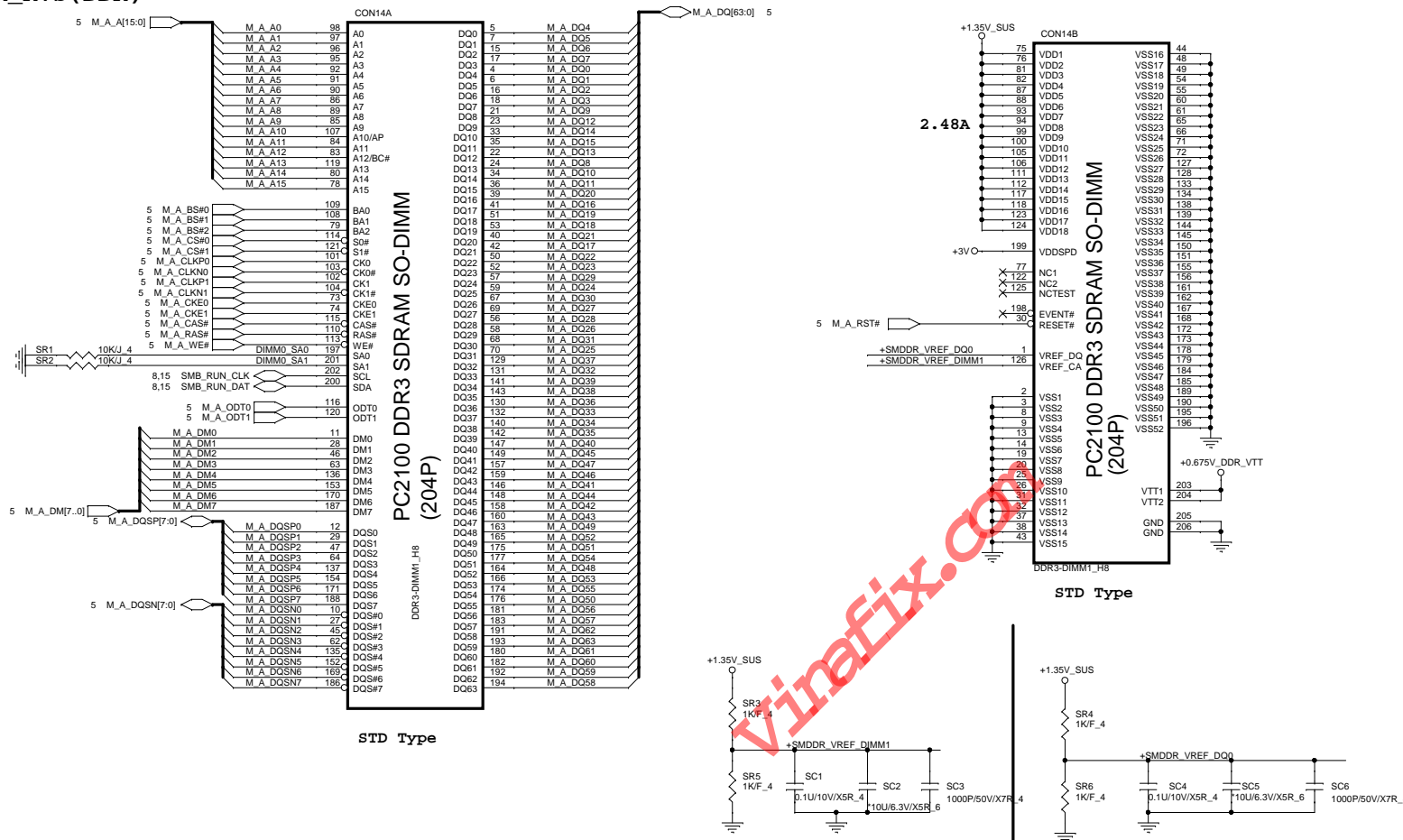


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**PROJECT : HKB**

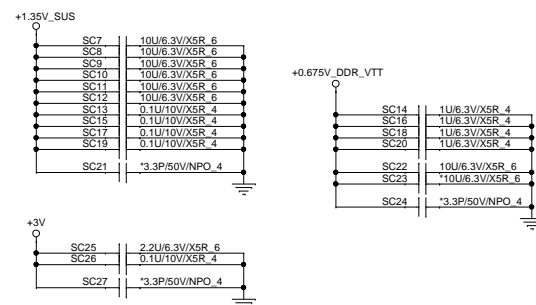
|                     |                         |                |
|---------------------|-------------------------|----------------|
| Size                | Document Number         | Rev            |
|                     |                         | 1A             |
| <b>FCH 6/6(GND)</b> |                         |                |
| Date:               | Tuesday, April 23, 2013 | Sheet 13 of 36 |

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DDR\_RVS (DDR)

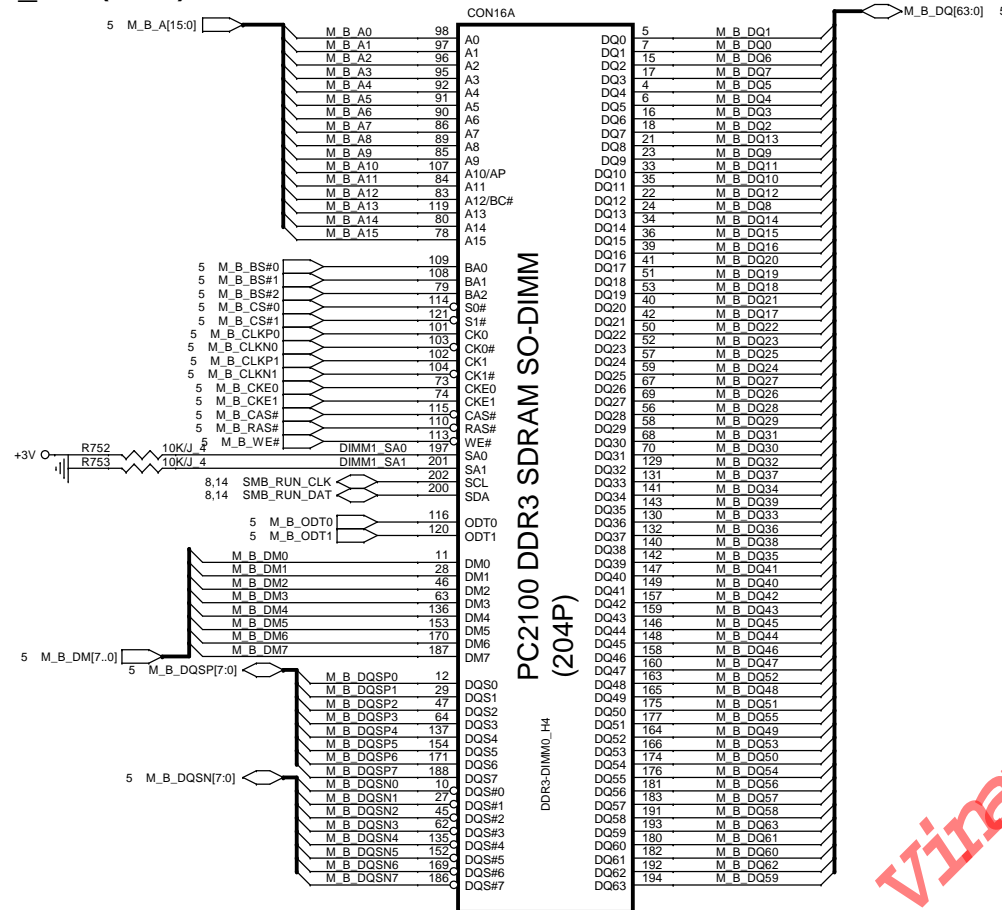


**Place these Caps near So-Dimm1.**



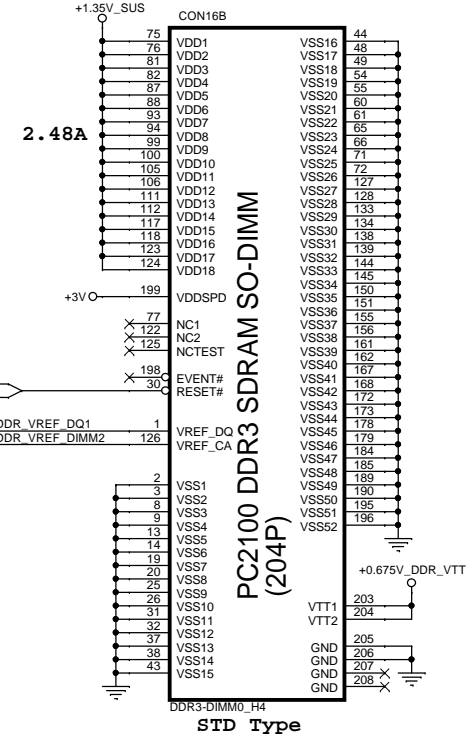
- 1.Level 1 Environment-related Substances Should Never be Used.
- 2.Recycled Resin and Coated Wire should be procured from Green Partners

# DDR\_STD (DDR)

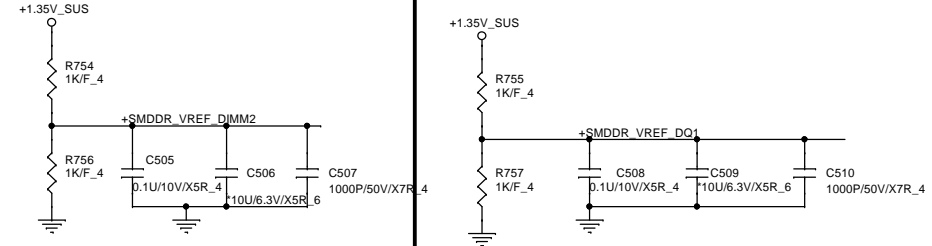


STD Type

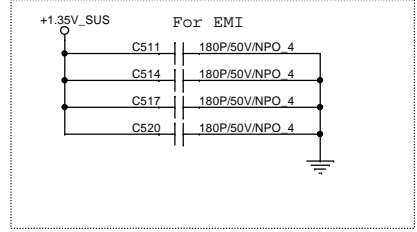
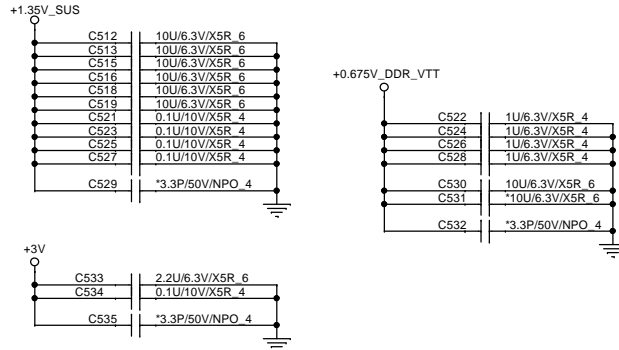
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STD Type



Place these Caps near So-Dimm1.



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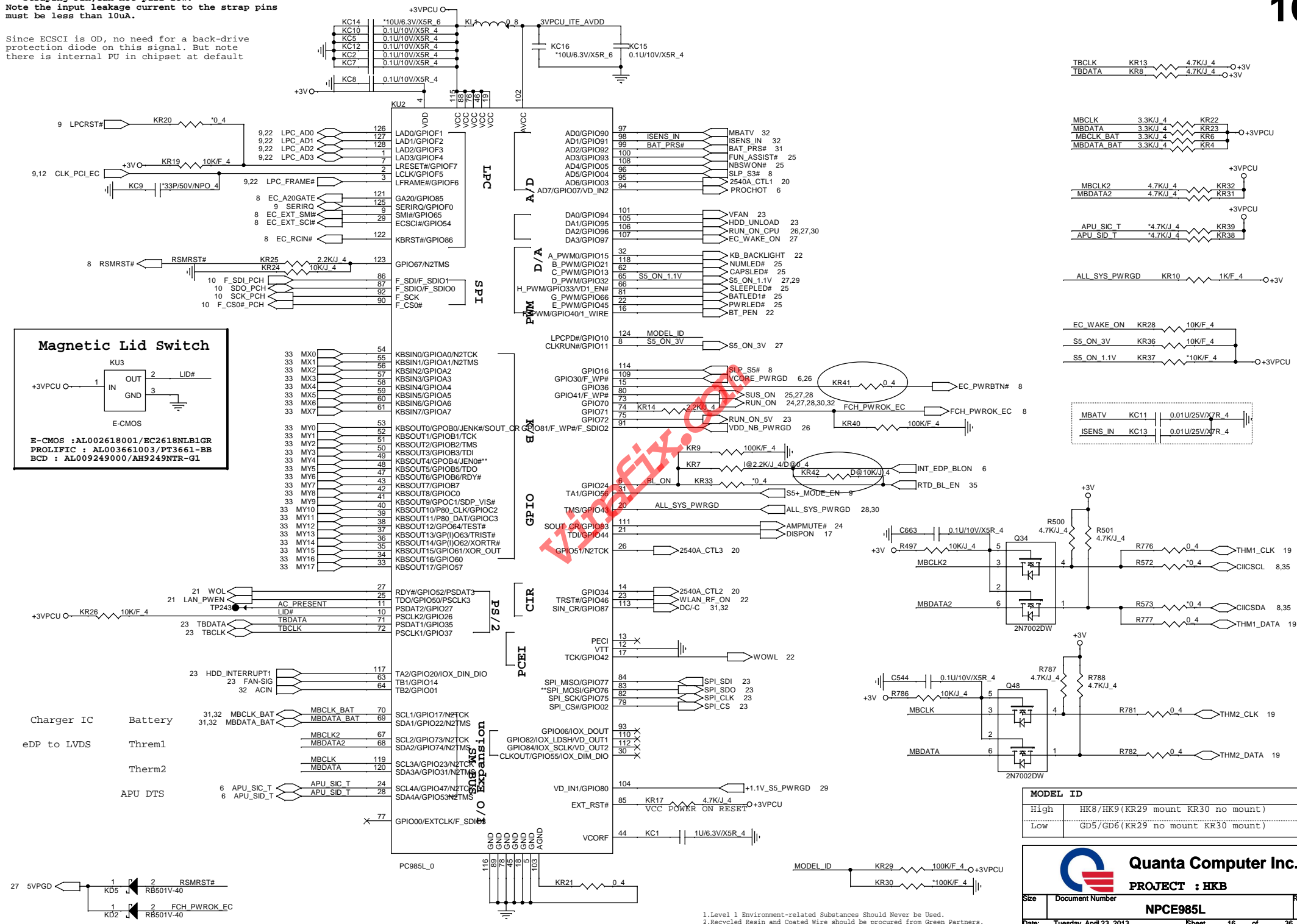
| Size | Document Number         | Rev            |
|------|-------------------------|----------------|
|      | DDRIII SO-DIMM-1        | 1A             |
| Date | Tuesday, April 23, 2013 | Sheet 15 of 36 |

1.Level 1 Environment-related Substances Should Never be Used.  
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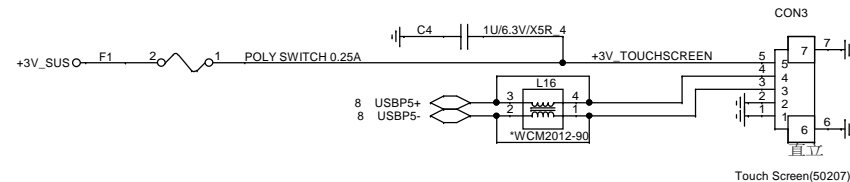
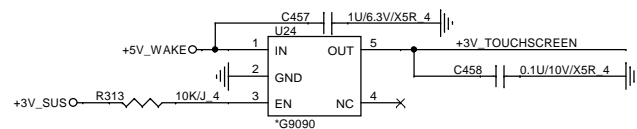


\*\* Strapping Pin, Can not pull low.  
Note the input leakage current to the strap pins  
must be less than 10uA.

Since ECSCI is OD, no need for a back-drive protection diode on this signal. But note there is internal PU in chipset at default



## Touch Screen



Add for EMI

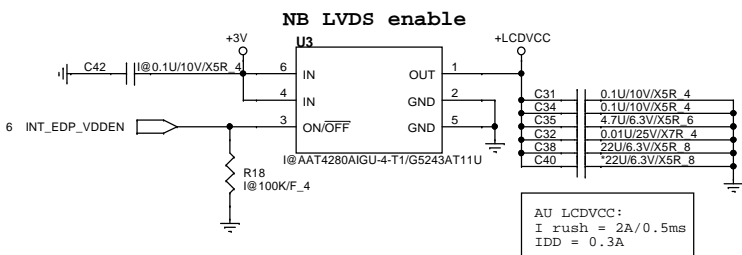
Camera HD specification  
Voltage: Max. 3.6V  
Current: Max. 200mA  
OCP: 200mA ~ 300mA

Distance between C268,C269,C270,C271  
and CON5 must larger than 500 mils

Close to connector

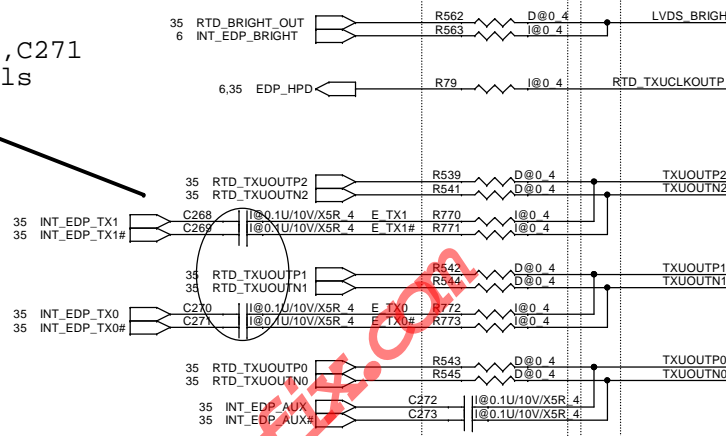
FAST, UL/CSA

LVDS

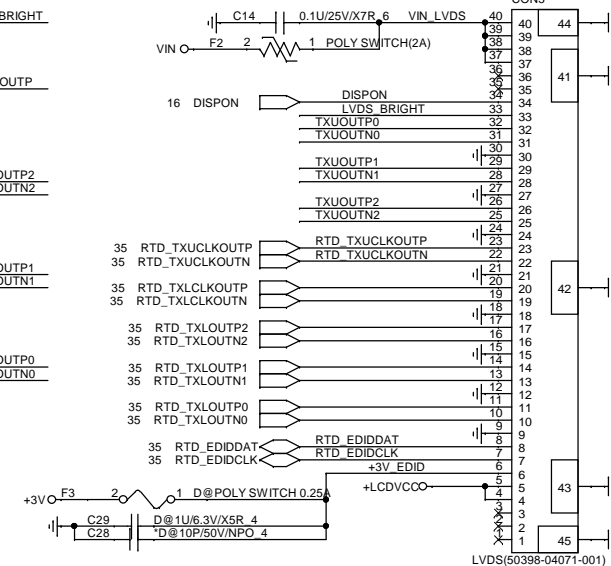


NB LVDS enable

AU LCDVCC:  
I rush = 2A/0.5ms  
IDD = 0.3A



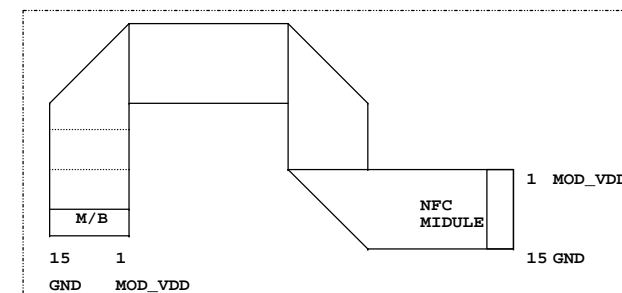
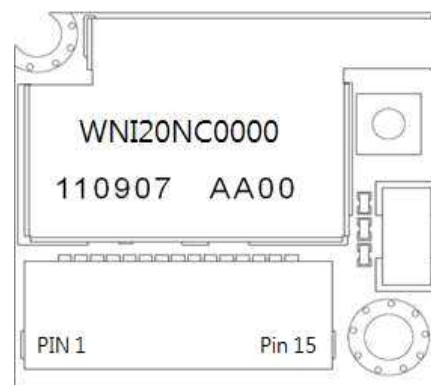
Branches are as short as possible!!



For EMI.  
Close to CON20

|             |      |                |
|-------------|------|----------------|
| NFC_DETECT# | C813 | *10P/50V/COG 4 |
| NFC-IRQ     | C814 | *10P/50V/COG 4 |
| SMB_NFC_CLK | C815 | *10P/50V/COG 4 |
| SMB_NFC_DAT | C816 | *10P/50V/COG 4 |

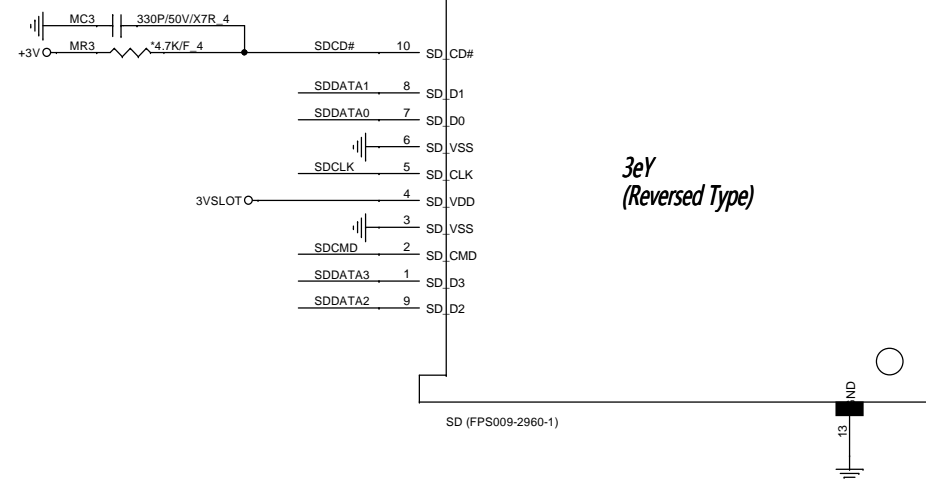
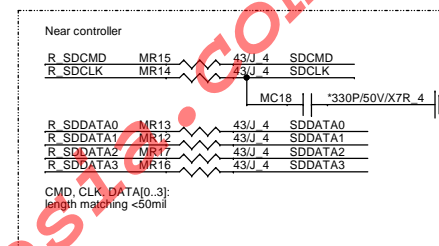
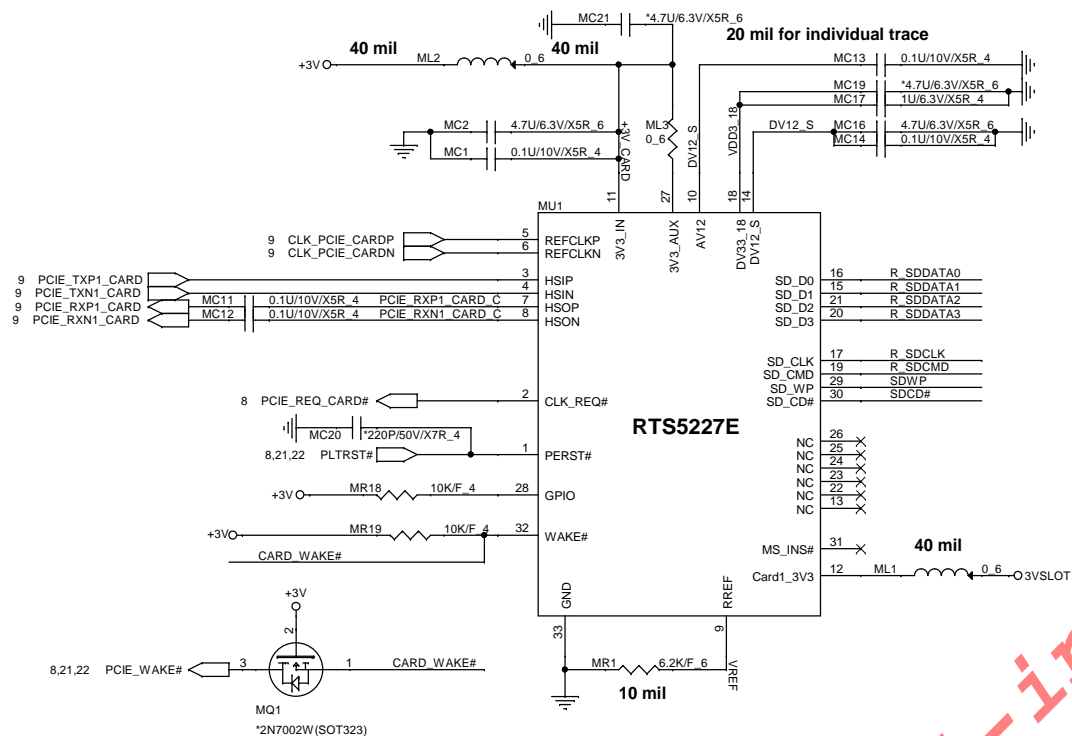
## NFC



NFC module :  
Vender : Samsung SNC-i20  
Power consumption : Max. 160mW/48mA  
Power Ripple +/- 50mV



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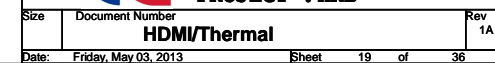
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PROJECT : HKB

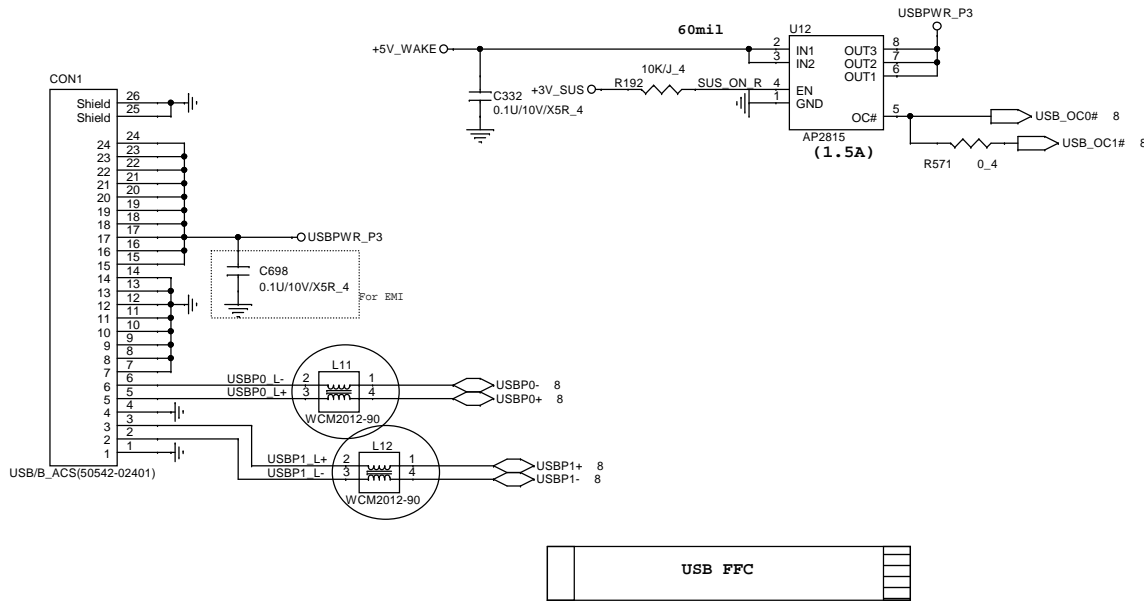
Card Reader(RTS5227E)



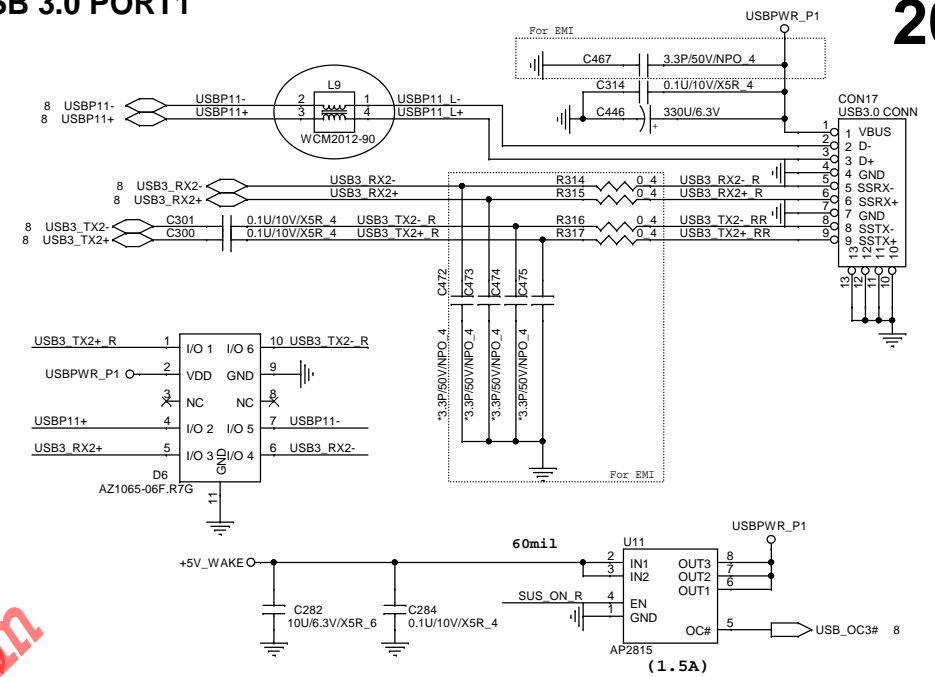
## Close to FIN



## MB to USB board



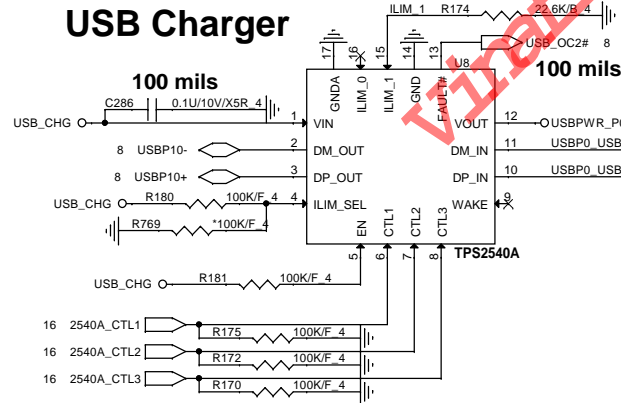
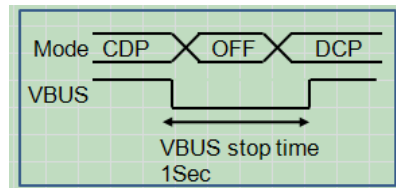
## USB 3.0 PORT1



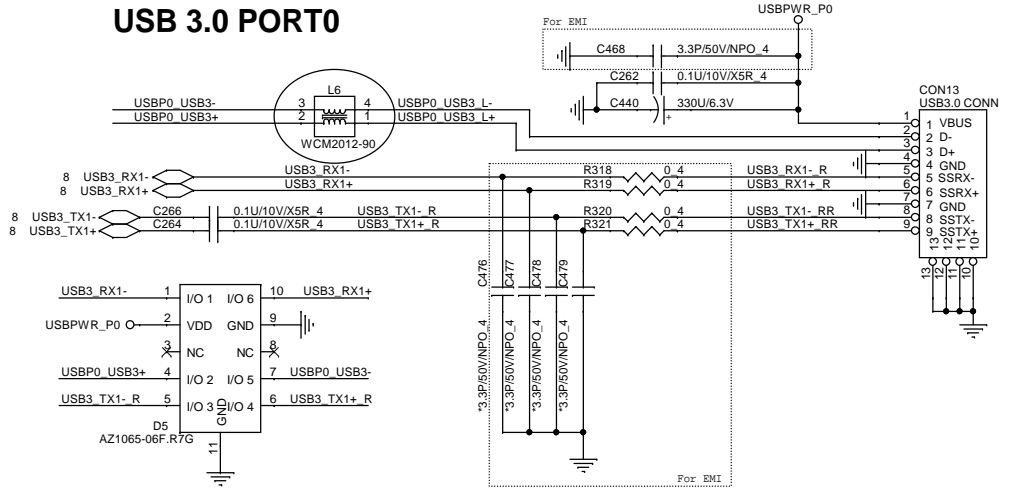
20

## USB Charger

SDP : Standard Downstream Port  
CDP : Charging downstream port  
DCP : Dedicated Charging Port  
Enable/Disable : setting by BIOS



## USB 3.0 PORT0



| CTL_1 | CTL_2 | CTL_3 | TPS 2540A/2543 Truth Table                        |
|-------|-------|-------|---|
| 0     | 0     | 0     | OUT discharge, power switch OFF                   |
| 0     | X     | 1     | DCP, Auto-detect(S3/S4/S5, 1.5A)                  |
| X     | 1     | 0     | SDP, USB2.0 mode(S0, 0.5A)                        |
| 1     | 0     | 0     | DCP, BC SPEC1.2 only(S3/Deep standby/S4/S5, 1.5A) |
| 1     | 0     | 1     | DCP, Divider mode only(S3/S4/S5, 1.5A)            |
| 1     | 1     | 1     | CDP (S0, 1.5A)                                    |

| System State | USB Battery Charging Setting |          |        |          |
|--------------|------------------------------|----------|--------|----------|
|              | Disable                      | C(1 2 3) | Enable | C(1 2 3) |
| S0           | SDP                          | (X 1 0)  | CDP    | (1 1 1)  |
| S3           | SDP                          | (X 1 0)  | DCP BC | (1 0 0)  |
| DS3          | Charger OFF                  | (0 0 0)  | DCP BC | (1 0 0)  |
| S4           | Charger OFF                  | (0 0 0)  | DCP BC | (1 0 0)  |
| S5           | Charger OFF                  | (0 0 0)  | DCP BC | (1 0 0)  |

| ILIM_SEL (I LIMIT(A)= 48000/R) |         |                    |
|--------------------------------|---------|--------------------|
| HI                             | I_LIM_1 |                    |
| LO                             | I_LIM_0 | 48000/22.6K=2.123A |



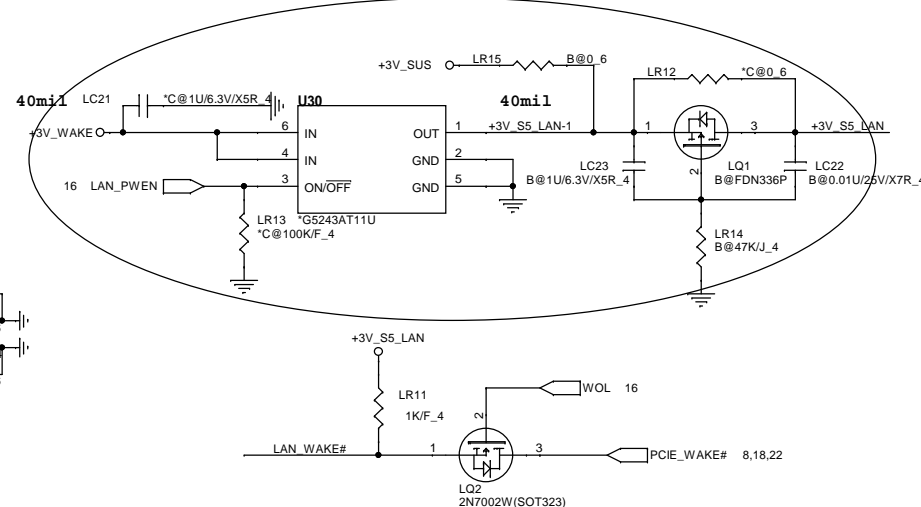
**Quanta Computer Inc.**  
PROJECT : HKB

USB/USB Charger

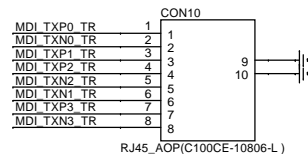
| Size | Document Number | Rev |
|------|-----------------|-----|
|      |                 | 1A  |

1. Lead-free Environment-related substances should never be used.  
2. Recycled Resin and Coated Wire should be procured from Green Partners.

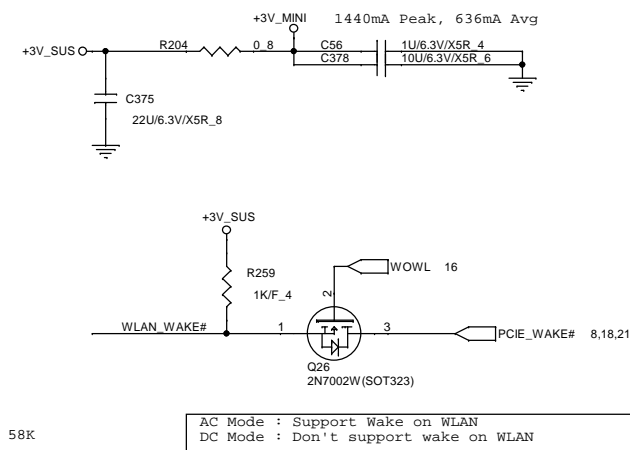
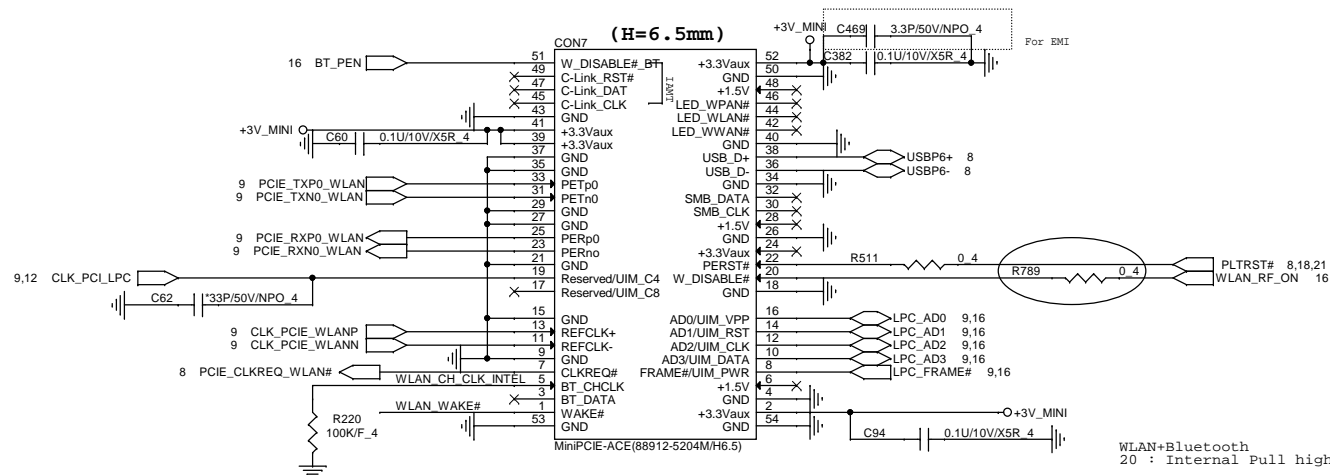
Date: Tuesday, April 23, 2013 Sheet 20 of 36



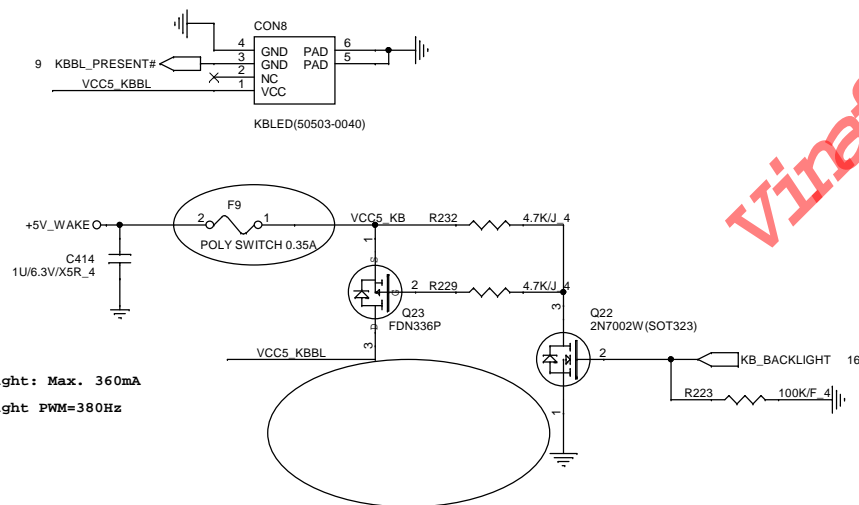
[vinfix.com](http://vinfix.com)



## WLAN/WIMAX/WIDI



**KB BACKLIGHT**

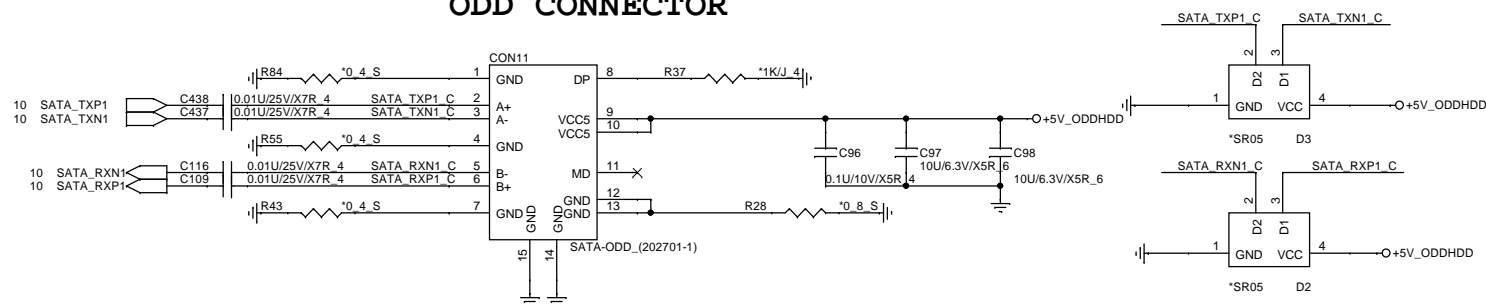


KB Backlight: Max. 360mA  
KB Backlight PWM=380Hz

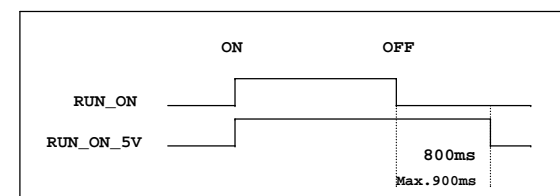
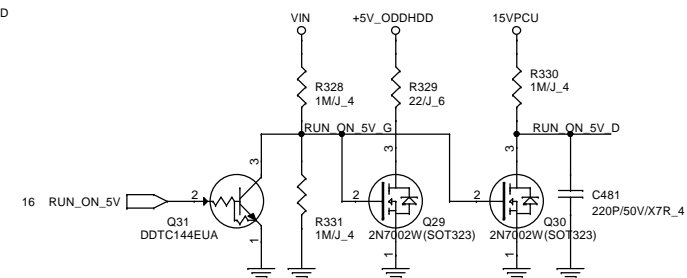
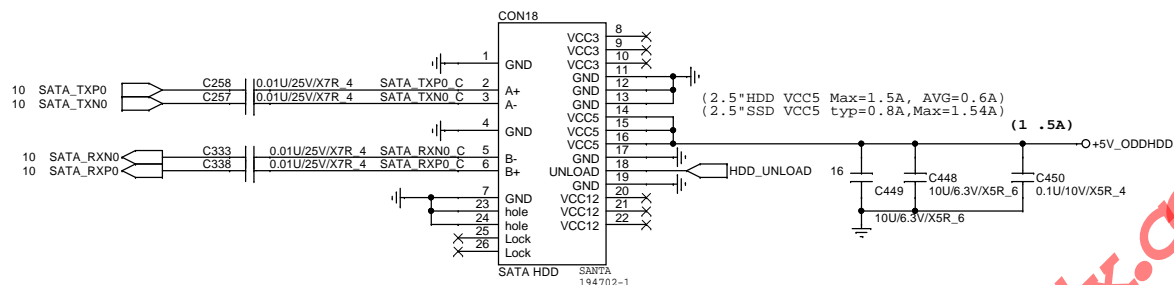




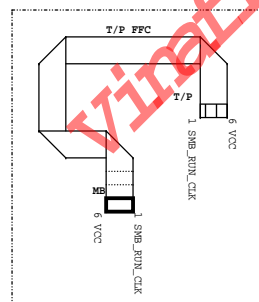
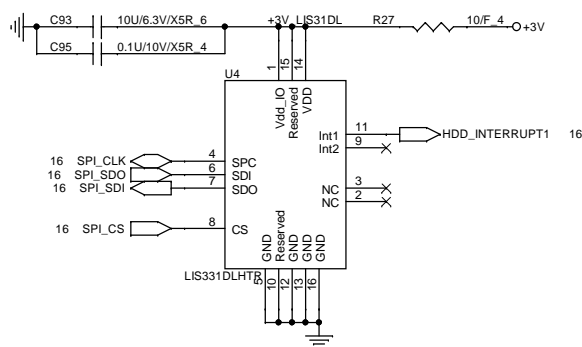
## ODD CONNECTOR



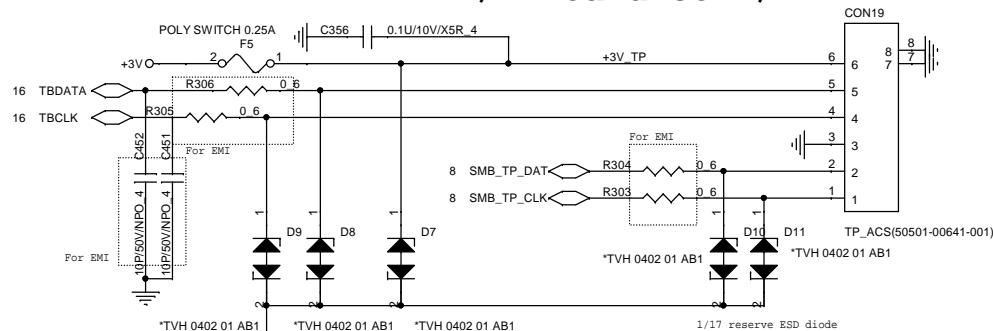
## HDD CONNECTOR



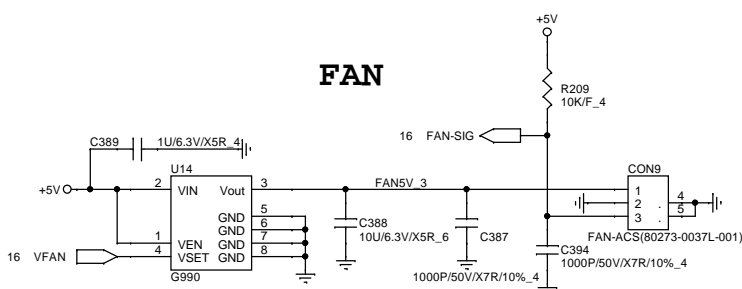
## HDD PROTECT SPI INTERFACE



## T/P Board to T/P

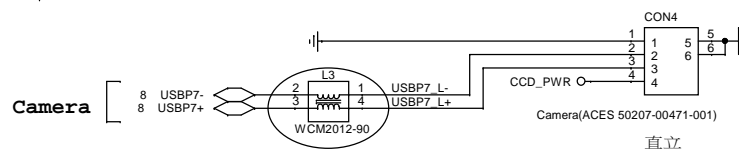
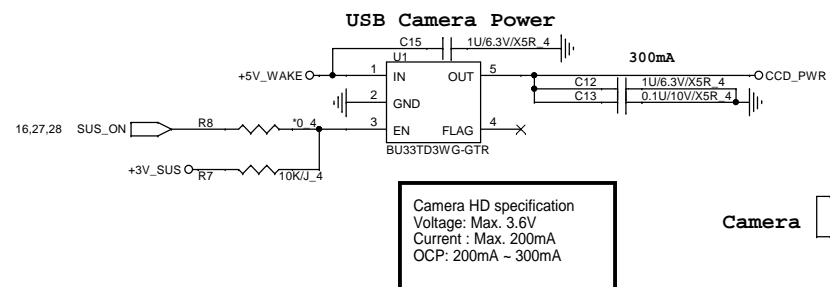


## FAN

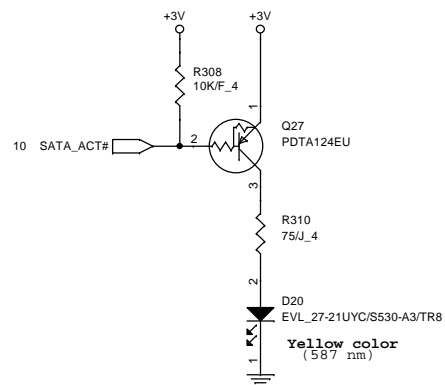




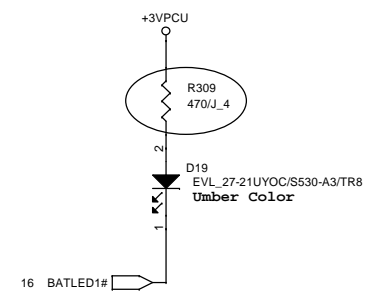
## Camera



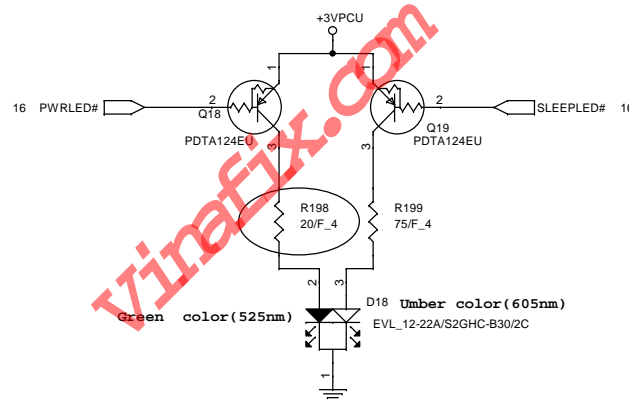
## SATA LED



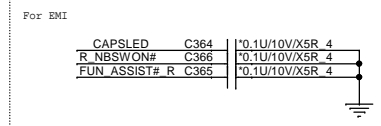
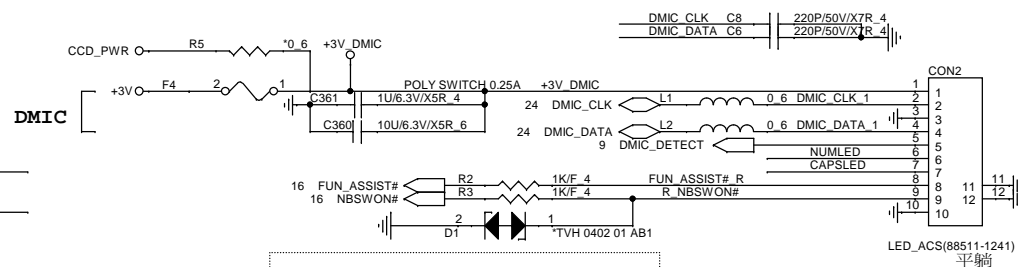
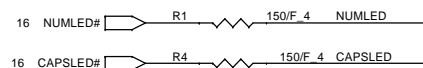
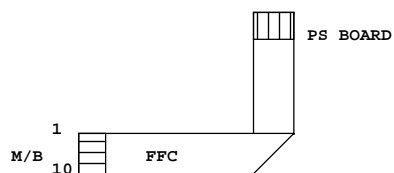
## BATTERY LED



## Power/Sleep LED



## Power SW Board Connector

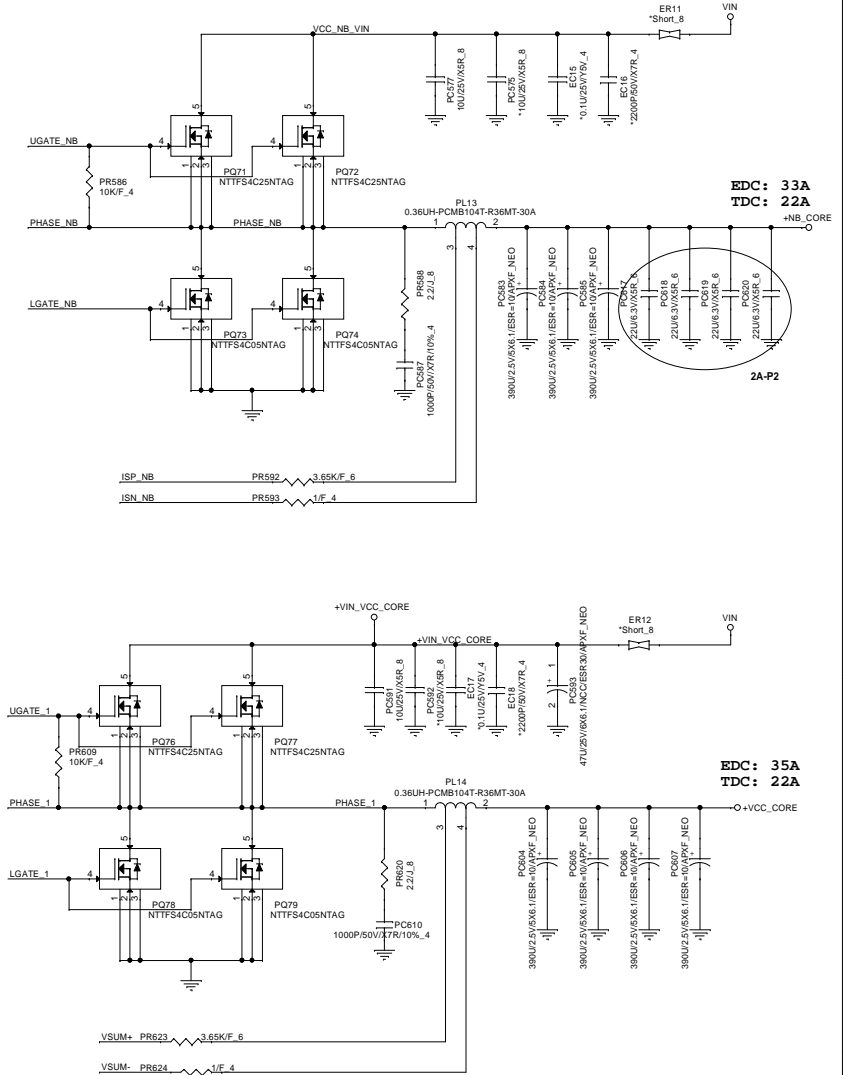
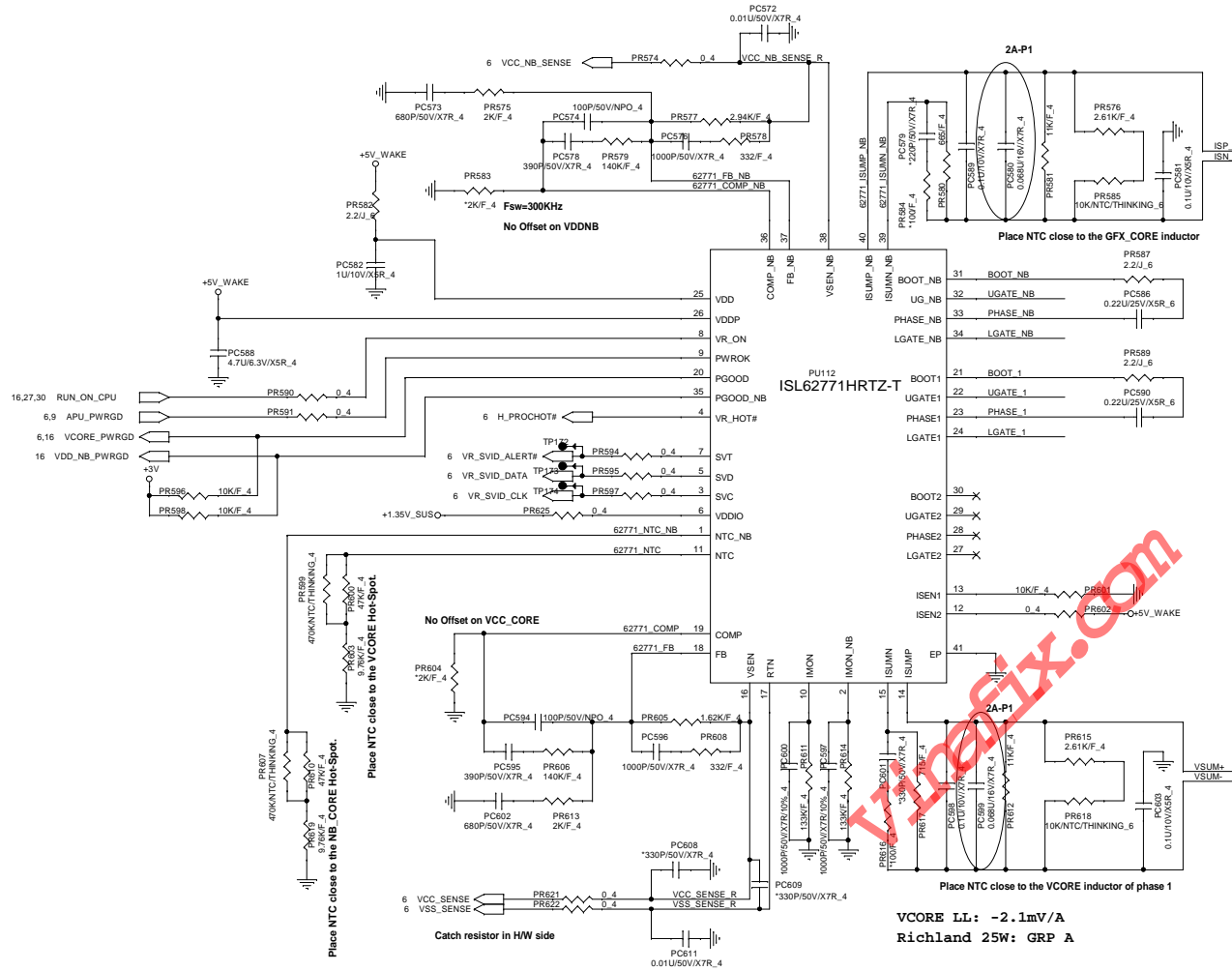


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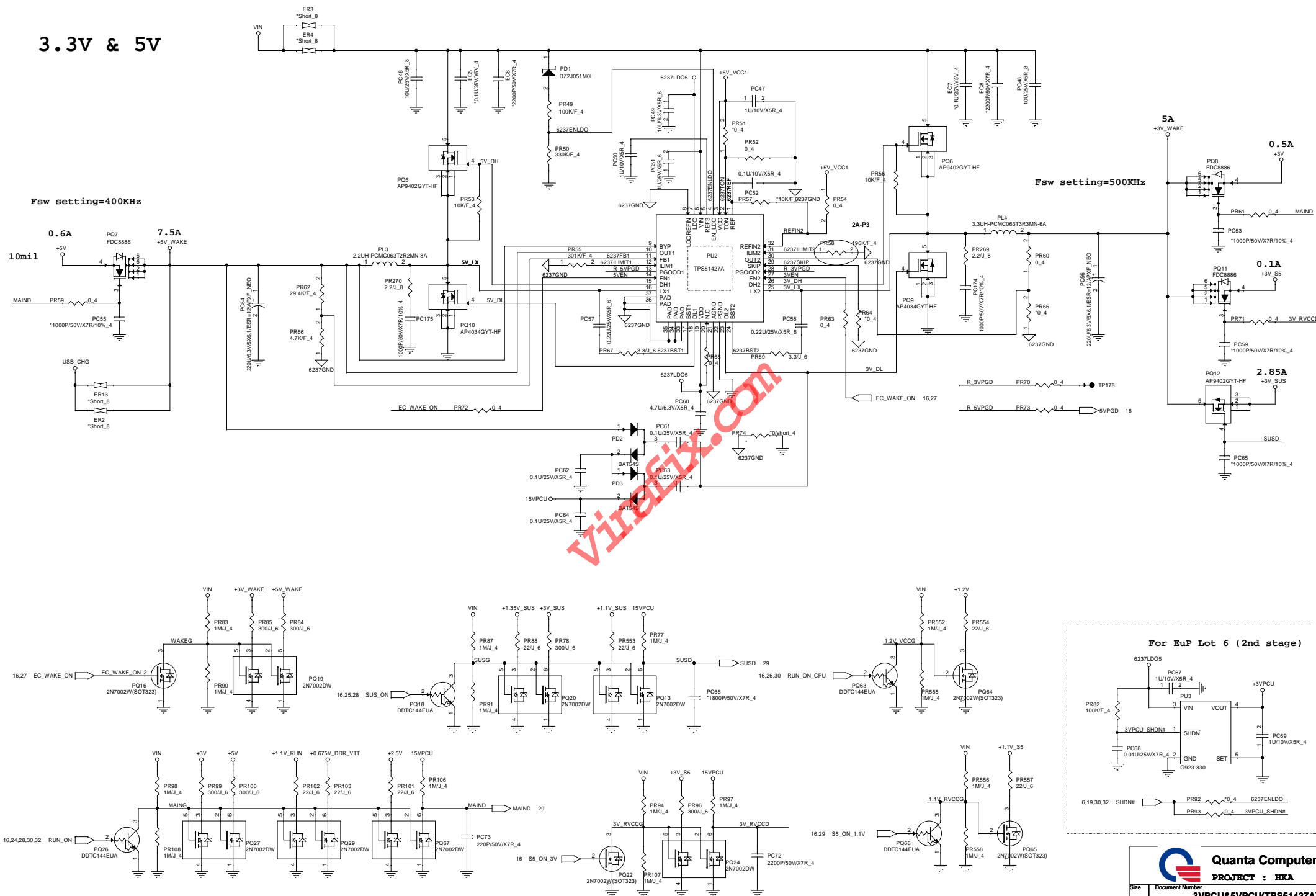
**PROJECT : HKB**

| Size  | Document Number           | Rev            |
|-------|---------------------------|----------------|
|       | <b>LED/PS/DMIC\Camera</b> | <b>1A</b>      |
| Date: | Tuesday, April 23, 2013   | Sheet 25 of 36 |

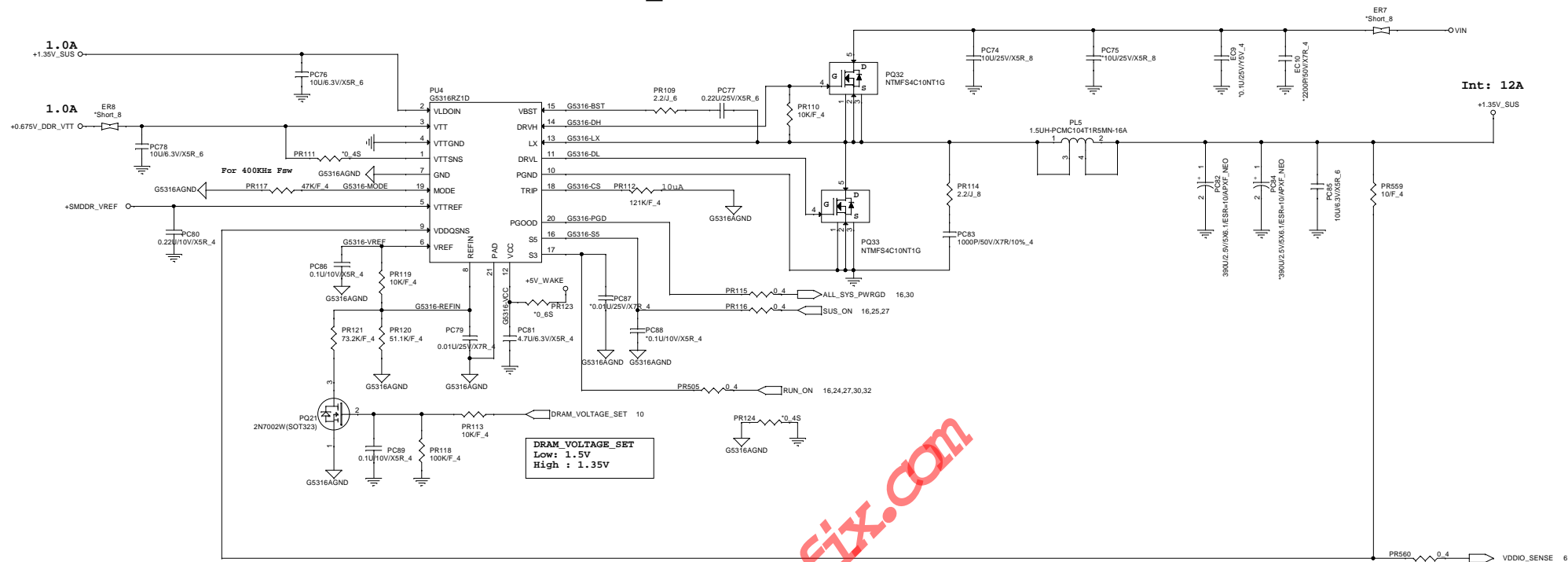
Richland 25W: GRP A  
VDD\_NB LL: -4.0mV/A



## 3.3V &amp; 5V



## 1.35VSUS &amp; VTT\_MEM



| MODE | Resistor on Mode | Fsw    | Discharge Mode         |
|------|------------------|--------|------------------------|
| 3    | 200Kohm          | 400KHz | Tracking discharge     |
| 2    | 100Kohm          | 300KHz |                        |
| 1    | 68Kohm           | 300KHz | Non-tracking discharge |
| 0    | 47Kohm           | 400KHz |                        |

| STATE | S3 | S5 | 1.35VSUS | VTTREF | VTT        |
|-------|----|----|----------|--------|------------|
| S0    | 1  | 1  | On       | On     | On         |
| S3    | 0  | 1  | On       | On     | Off/High Z |
| S4/S5 | 0  | 0  | Off      | Off    | Off        |

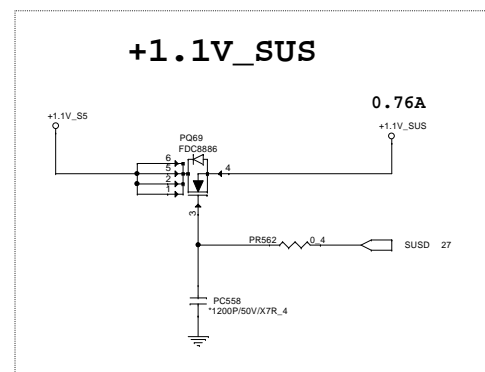
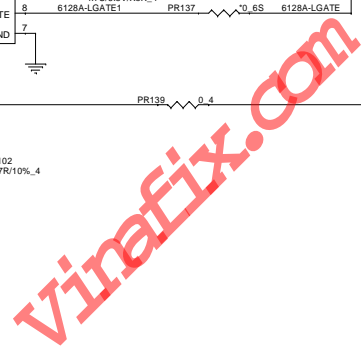


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PROJECT : HKA

| Size | Document Number  | Rev |
|------|------------------|-----|
|      | 1.35VSUS/VTT_MEM | 1A  |

1.Level 1 Environment-related Substances Should Never be Used.  
2.Recycled Resin and Coated Wire should be procured from Green Partners.

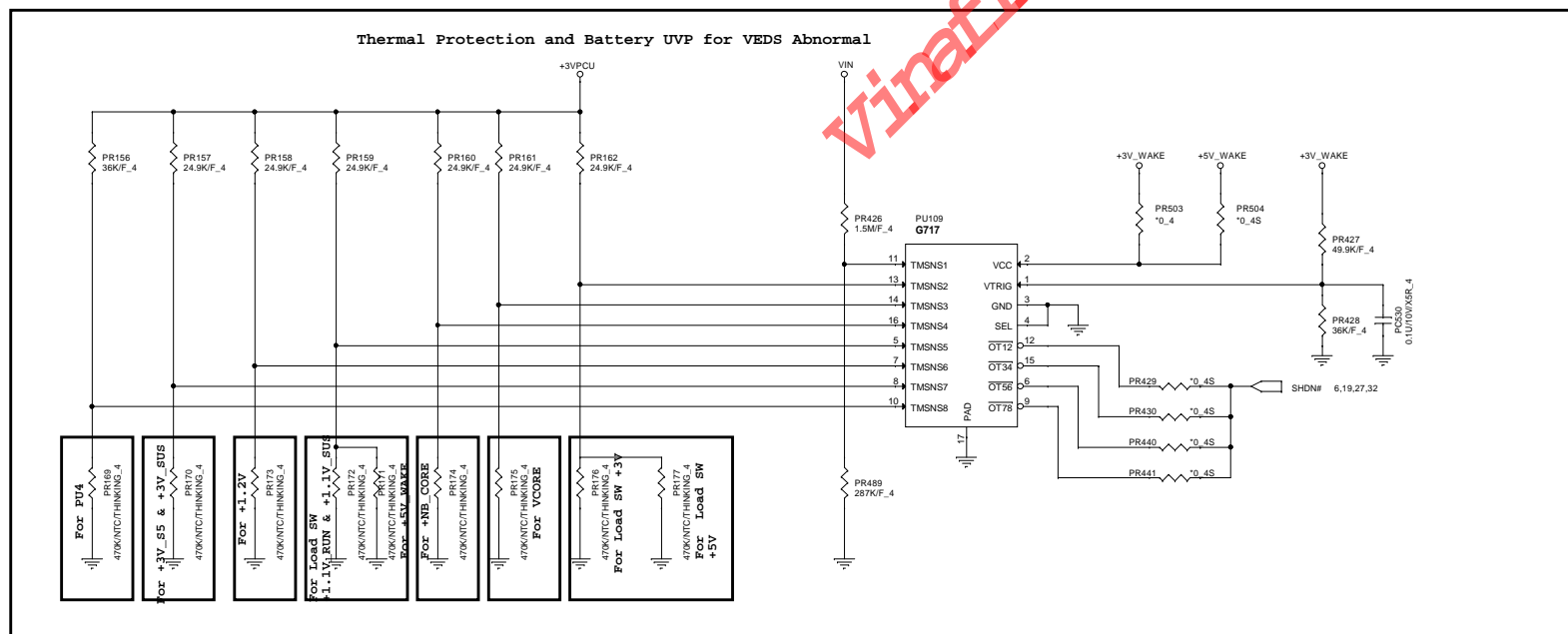
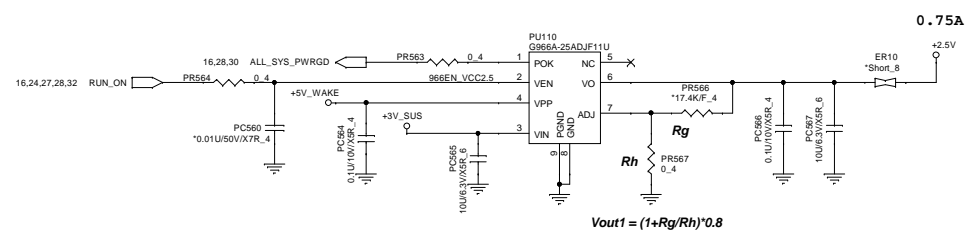
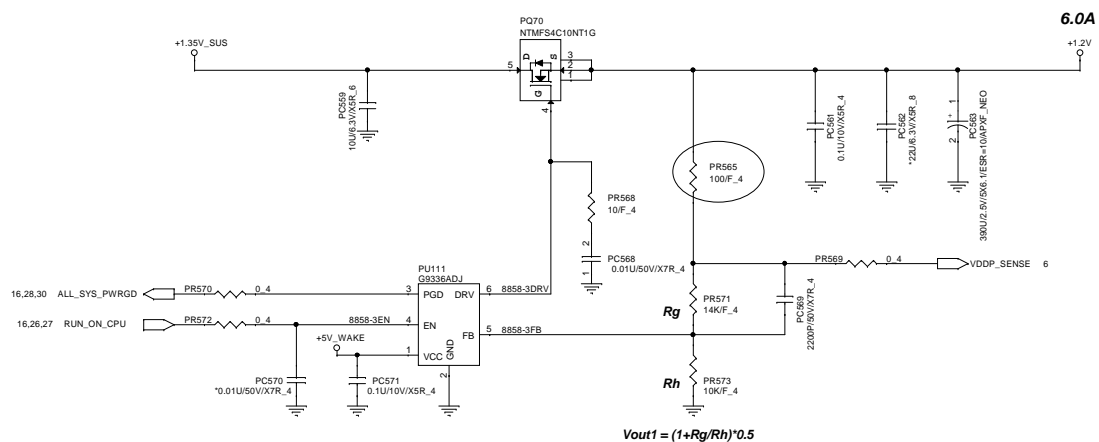
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VCC1.2

VCC2.5



31

31

31

**VIN Short Circuit Protection for ADP/BAT**

**BAT OVP circuit**

**Separate adapter OVP from UL circuit**

**System OVP for VEDS**

**UL Latch Circuit**

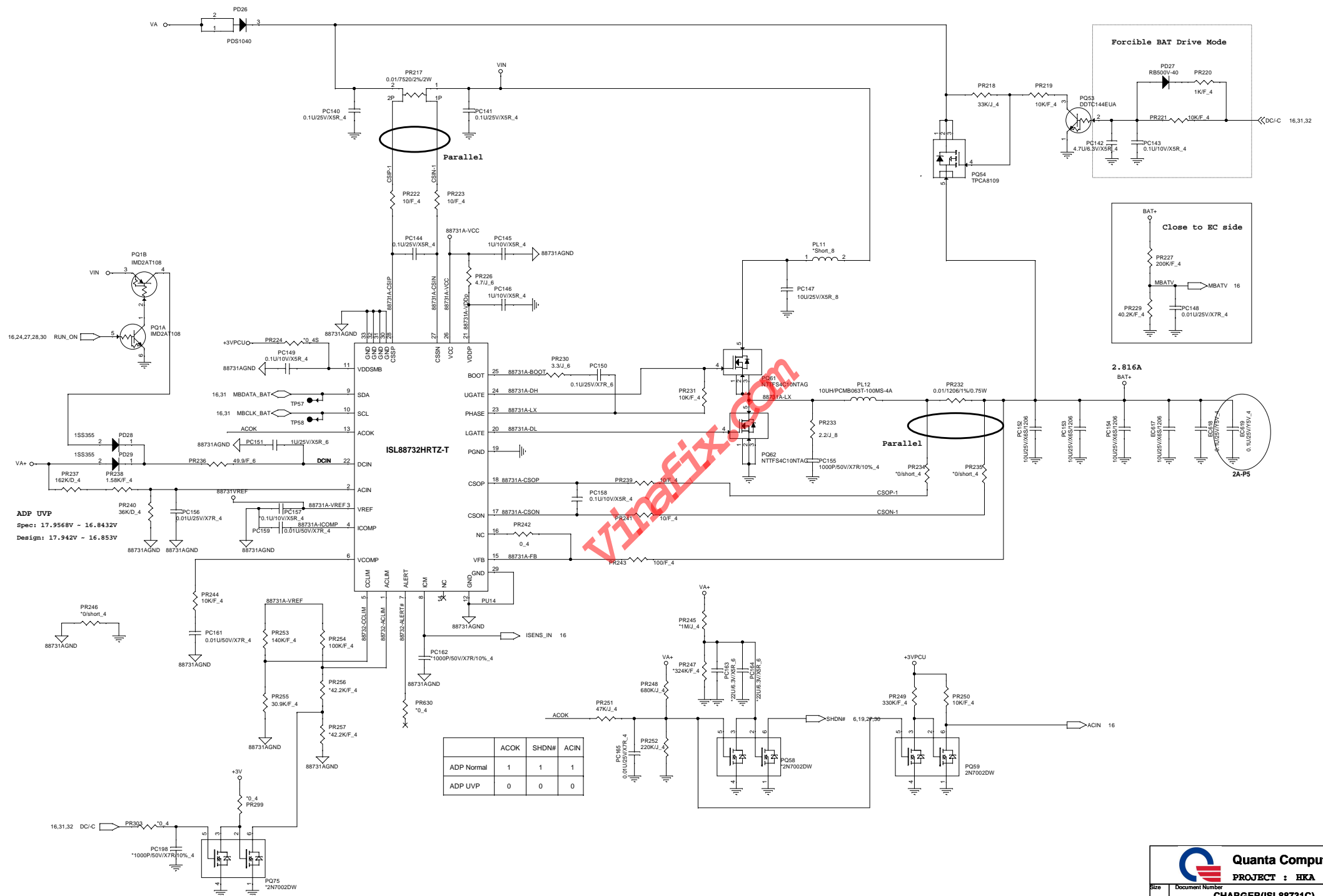
**BATTERY CONNECTOR**

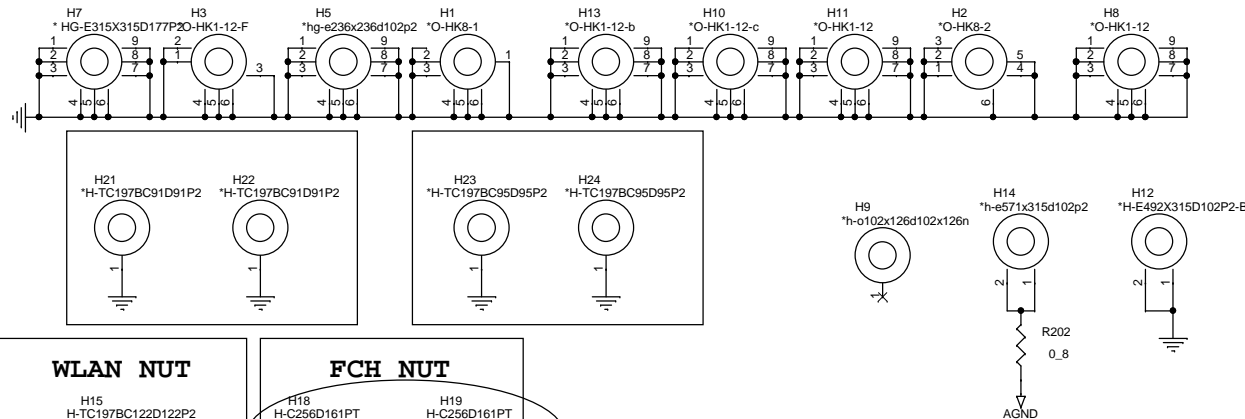
| Size | Document Number  | Rev |
|------|------------------|-----|
| 1    | BAT IN/ADA IN/UL | 1A  |

1. Level 1 Environment-related Substances should Never be Used.  
2. Recycled Resin and Coated Wire should be procured from Green Partners.

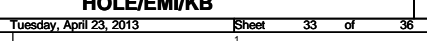
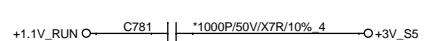
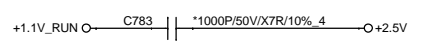
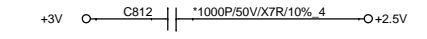
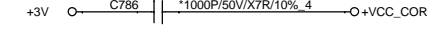
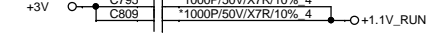
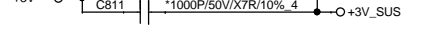
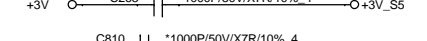
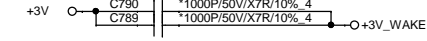
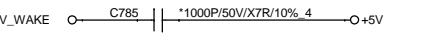
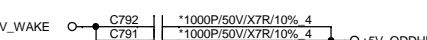
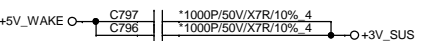
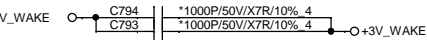
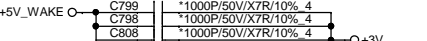
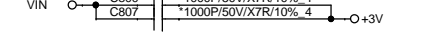
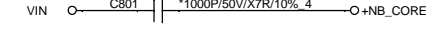
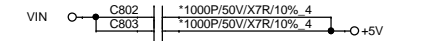
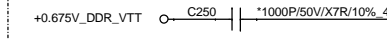
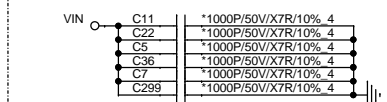
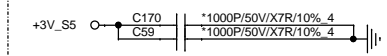
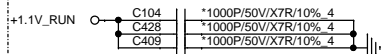
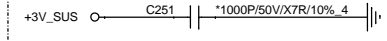
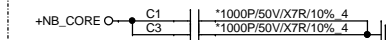
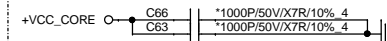
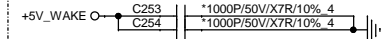
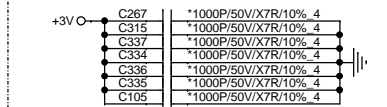
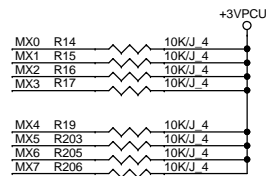
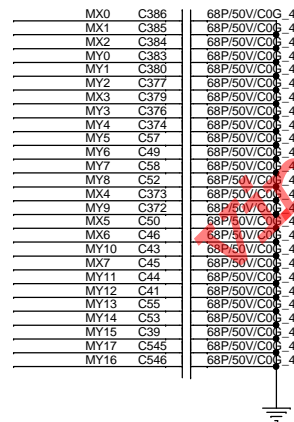
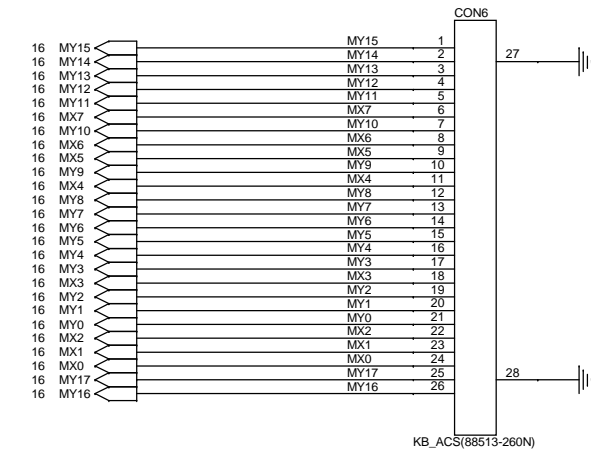
Date: Tuesday, April 23, 2013 Sheet 31 of 36

[illegible]





## KEY BOARD Connector

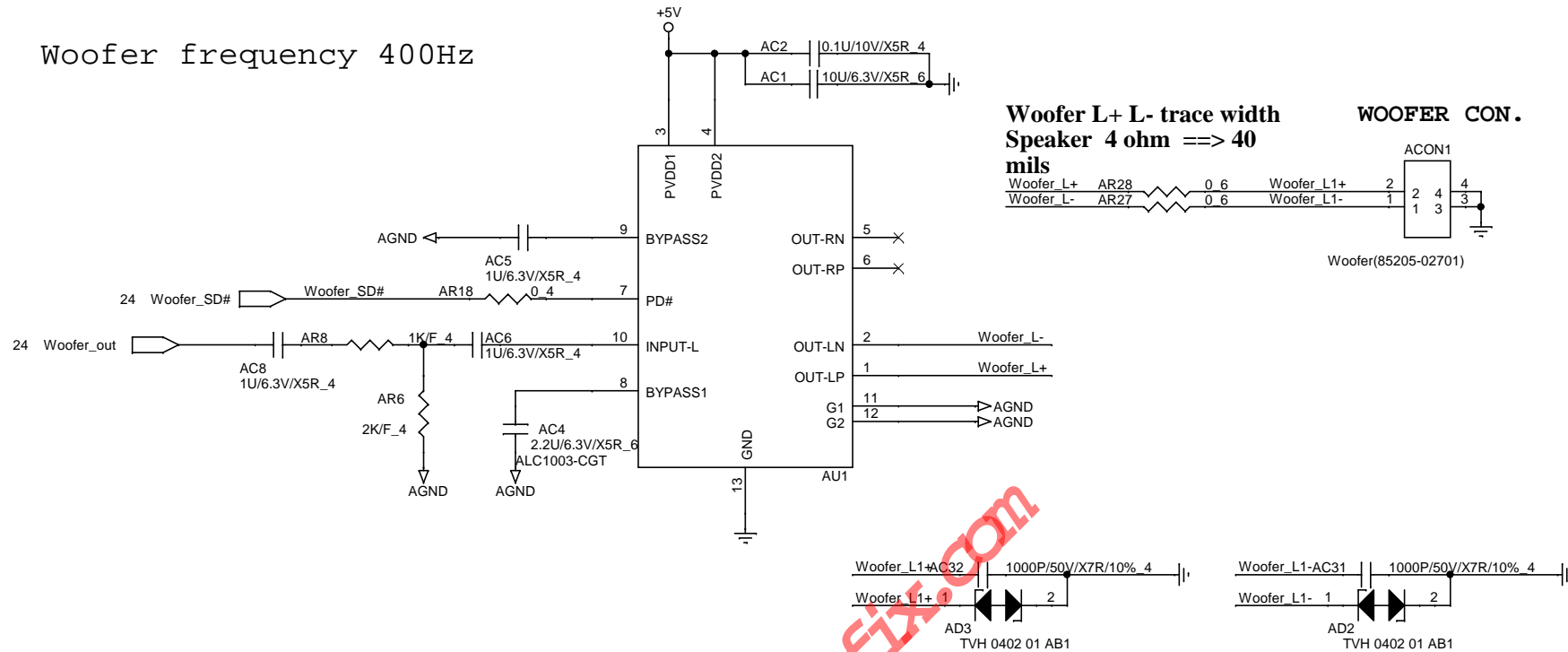


**Quanta Computer Inc.**  
PROJECT : HKA

|       |                         |                |
|-------|-------------------------|----------------|
| Size  | Document Number         | Rev            |
|       | HOLE/EMI/KB             | 1A             |
| Date: | Tuesday, April 23, 2013 | Sheet 33 of 36 |

1.Level 1 Environment-related Substances Should Never be Used.  
2.Recycled Resin and Coated Wire should be procured From Green Partners.

Woofers frequency 400Hz



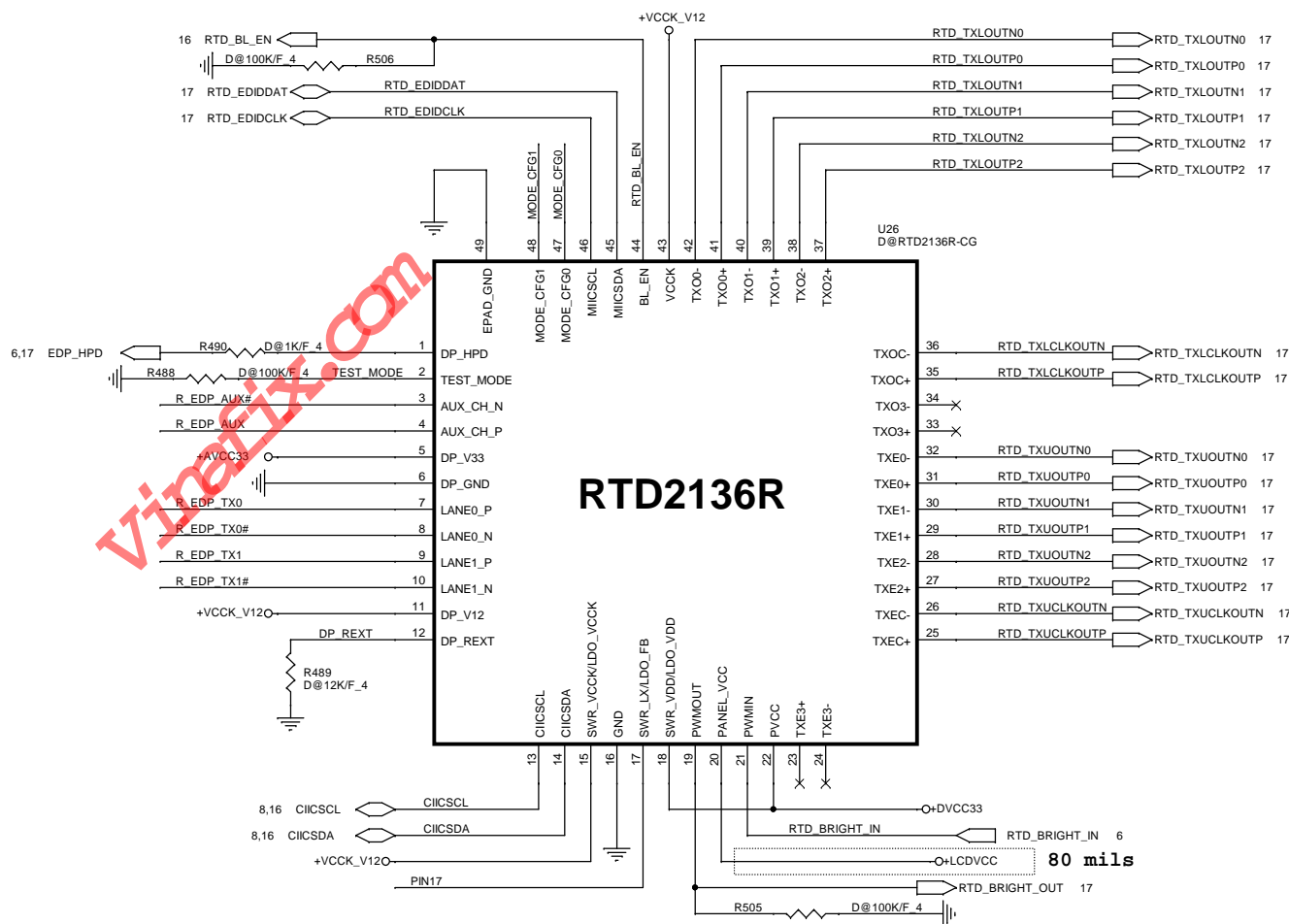
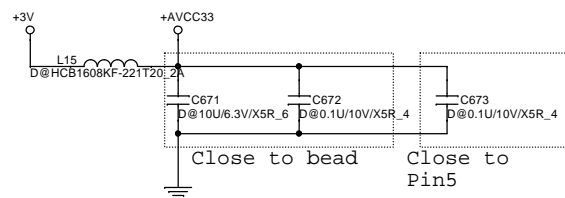
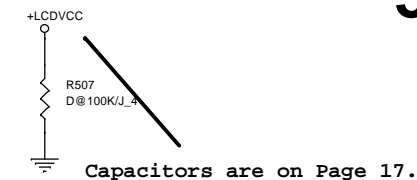
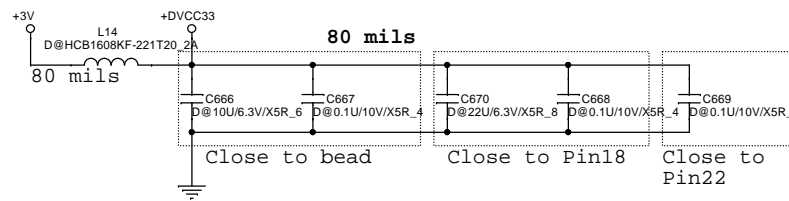
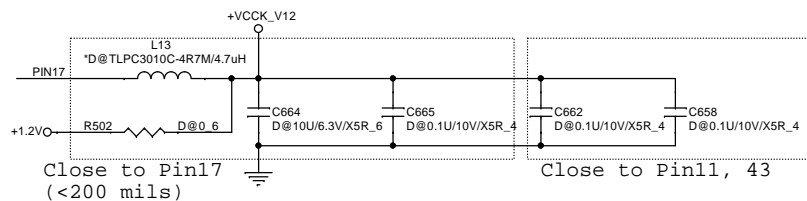
1.Level 1 Environment-related Substances Should Never be Used.  
2.Recycled Resin and Coated Wire should be procured from Green Partners.



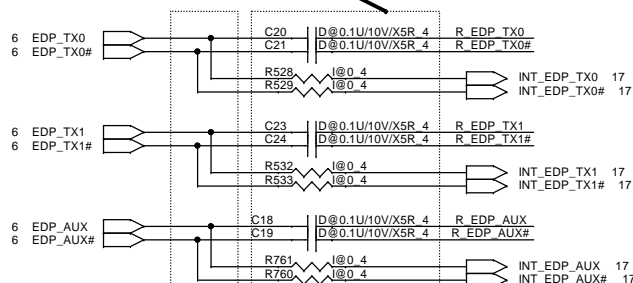
**Quanta Computer Inc.**

**PROJECT : HKB**

| Size  | Document Number            | Rev            |
|-------|----------------------------|----------------|
|       | <b>Woofer(ALC1003-CGT)</b> | 1A             |
| Date: | Tuesday, April 23, 2013    | Sheet 34 of 36 |



Close to RTD2136R



Branches are as short as possible

|                  |   | MODE_CFG0(PIN47) |             |
|------------------|---|------------------|-------------|
|                  |   | 0                | 1           |
| MODE_CFG1(PIN48) | 0 | X                | EP MODE     |
|                  | 1 | ROM ONLY MODE    | EEPROM MODE |

1. Level 1 Environment-related Substances Should Never be Used.  
2. Recycled Resin and Coated Wire should be procured from Green Partners.



Quanta Computer Inc.

PROJECT : HKB

eDP to LVDS

## I/O PORT LIST

| USB PORT Architecture |              |
|-----------------------|--------------|
| PORT 0                | USB2.0       |
| PORT 1                | USB2.0       |
| PORT 2                | N/A          |
| PORT 3                | N/A          |
| PORT 4                | N/A          |
| PORT 5                | Touch Screen |
| PORT 6                | WiMAX/BT     |
| PORT 7                | Camera       |
| PORT 8                | N/A          |
| PORT 9                | N/A          |
| PORT 10               | USB3.0       |
| PORT 11               | USB3.0       |
| PORT 12               | N/A          |
| PORT 13               | N/A          |

| PCIE BUS |                 |
|----------|-----------------|
| PORT 0   | WLAN Port       |
| PORT 1   | CARD READER     |
| PORT 2   | GLAN(RTL8111GS) |
| PORT 3   | N/A             |

| SATA BUS |     |
|----------|-----|
| PORT 0   | HDD |
| PORT 1   | ODD |
| PORT 2   | N/A |
| PORT 3   | N/A |
| PORT 4   | N/A |
| PORT 5   | N/A |

## M/B ID LIST

| SIZE    | Board ID0 |
|---------|-----------|
| HKA 14" | 0         |
| HKB 15" | 1         |

| I/F  | Board ID1 |
|------|-----------|
| eDP  | 0         |
| LVDS | 1         |

| CPU | Board ID2 | Board ID3 |
|-----|-----------|-----------|
| A4  | 0         | 0         |
| A6  | 0         | 1         |
| A8  | 1         | 0         |
| A10 | 1         | 1         |

## SM BUS LIST

| SM BUS         | MBCLK/MBDATA | WRITE     | READ      | Function |
|----------------|--------------|-----------|-----------|----------|
| ISL88732HRTZ-T | 0001 001X    | 0001 0010 | 0001 0011 | Charger  |

| SM BUS     | MBCLK_BAT/MBDATA_BAT | WRITE     | READ      | Function |
|------------|----------------------|-----------|-----------|----------|
| VGP-BPS35A | 0001 011X            | 0001 0110 | 0001 0111 | Battery  |

| SM BUS        | SMB0_RUN_CLK/SMB0_RUN_DAT | WRITE     | READ      | Function |
|---------------|---------------------------|-----------|-----------|----------|
| DIMM Module 0 | 1010 000X                 | 1010 0000 | 1010 0001 | DDRIII   |
| DIMM Module 1 | 1010 010X                 | 1010 0100 | 1010 0101 | DDRIII   |

| SM BUS    | SMB1_RUN_CLK/SMB1_RUN_DAT | WRITE     | READ      | Function  |
|-----------|---------------------------|-----------|-----------|-----------|
| Synaptics | 0010 110X                 | 0010 1100 | 0010 1101 | Click PAD |

## POWER MAP

|                | S0 | S3 | S4   | S5<br>(Charger Enable) | S5<br>(Charger Disable) |
|----------------|----|----|------|------------------------|-------------------------|
| RUN_ON_CPU     | H  | L  | L    | L                      | L                       |
| +NB_CORE       | H  | L  | L    | L                      | L                       |
| +VCC_CORE      | H  | L  | L    | L                      | L                       |
| +1.2V          | H  | L  | L    | L                      | L                       |
| RUN_ON         | H  | L  | L    | L                      | L                       |
| +5V            | H  | L  | L    | L                      | L                       |
| +3V            | H  | L  | L    | L                      | L                       |
| +2.5V          | H  | L  | L    | L                      | L                       |
| +1.1V_RUN      | H  | L  | L    | L                      | L                       |
| +0.75V_DDR_VTT | H  | L  | L    | L                      | L                       |
| SUS_ON         | H  | H  | L    | L                      | L                       |
| +1.35V_SUS     | H  | H  | L    | L                      | L                       |
| +3V_SUS        | H  | H  | L    | L                      | L                       |
| +1.1V_SUS      | H  | H  | L    | L                      | L                       |
| S5_ON_1.1V     | H  | H  | *H/L | L                      | L                       |
| +1.1V_S5       | H  | H  | *H/L | L                      | L                       |
| S5_ON_3V       | H  | H  | *H/L | L                      | L                       |
| +3V_S5         | H  | H  | *H/L | L                      | L                       |
| EC_WAKE_ON     | H  | H  | *H/L | H                      | L                       |
| +3V_WAKE       | H  | H  | *H/L | H                      | L                       |
| +5V_WAKE       | H  | H  | *H/L | H                      | L                       |
| RUN_ON_5V      | H  | L  | L    | L                      | L                       |
| +5V_ODDHDD     | H  | L  | L    | L                      | L                       |

\* H: If wake up event exists.